

C0402C102J1HACTU

General Information

Aliases (C0402C102J1HAC7867) SMD Comm X8R HT150C, Ceramic, 1,000 pF, 5%, 100 VDC, X8R, SMD, MLCC, High Temperature, Ultra-Stable, 0402, 0.3 mm



Click here for the 3D model.

Series SMD Comm X8R HT150C Style SMD Chip Description SMD, MLCC, High Temperature, Ultra-Stable Features High Temperature, Ultra-Stable RoHS Yes Termination Tin Marking No AEC-Q200 No **Typical Component Weight** 1.21 mg Shelf Life 78 Weeks MSL 1

	Specifications	
	Capacitance	1,000 pF
	Measurement Condition	1 MHz 1.0Vrms
	Tolerance	5%
	Voltage DC	100 VDC
	Dielectric Withstanding Voltage	250 VDC
	Temperature Range	-55/+150°C
	Temp. Coefficient	X8R
	Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC)	15%, 1MegaHz 1.0Vrms
	Dissipation Factor	2.5% 1 MHz 1.0Vrms
	Aging Rate	0% Loss/Decade Hour: Referee Time is 1000 Hours
	Insulation Resistance	100 GOhms

 Dimensions

 Chip Size
 0402

 L
 1mm +/-0.05mm

 W
 0.5mm +/-0.05mm

 T
 0.5mm +/-0.05mm

 S
 0.3mm MIN

 B
 0.3mm +/-0.1mm

Packaging Specifications

Packaging	T&R, 180mm, Paper Tape
Packaging Quantity	10000

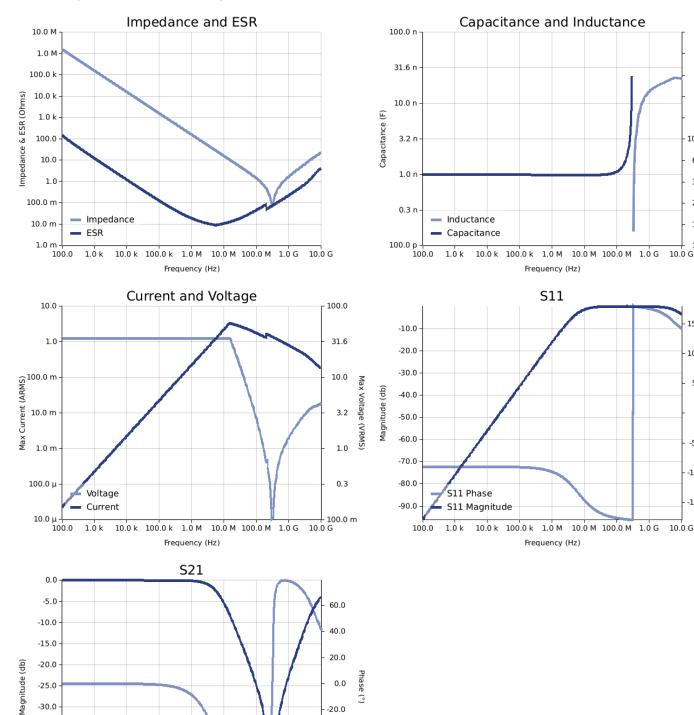
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Simulations

For the complete simulation environment please visit K-SIM.



-40.0

-60.0

-80.0

10.0 G

100.0

-35.0

-40.0

-45.0

-50.0

S21 Phase

S21 Magnitude

1.0 k 10.0 k 100.0 k 1.0 M 10.0 M 100.0 M 1.0 G

Frequency (Hz)

1.0 n

0.6 n

0.4 n

0.3 n

100.0 p

0.2 n n tan

63.1 p 🗄

39.8 p

25.1 p

15.8 p

10.0 p

150.0

100.0

50.0

0.0

-50.0

-100.0

-150.0

10.0 G

Phase (°)



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These are simulations.

This is not a specification!

The responses shown represent the typical response for each part type. Specific responses may vary, depending on manufacturing variation affects of all parameters involved, including the specified tolerances applied to capacitance and unspecified variations of ESR, ESL, and leakage resistance.

The responses shown do not represent a specified or implied maximum capability of the device for all applications.

- The ESR used for ripple "Ripple Current/Voltage vs. Frequency" plots is the ESR at ambient temperature.

- The ESR used for hipple klipple current younge vs. requericy plots is the ESR at an bient temperature.
 The ESR in the "Temperature Rise vs. Ripple Current" plots is adjusted to each incremental temperature rise before the power and ripple current is calculated.
 The effects shown herein are based on measured data from a multiple part sample of the parts in question.
 Ripple capability of this device will be factored by thermal resistance (Rth) created by circuit traces (addi affects of all parameters involved, including the specified tolerances applied to capacitance and unspecified variations of ESR, ESL, and leakage resistance.
 The peak voltages generated in the "Temperature Rise vs. Combined Ripple Currents" plot are calculated for each frequency and are not combined with voltages are not combined with voltages.
- generated at any other harmonics. Please consult with the catalog or field applications engineer for maximum capability of the device in specific applications.

All product information and data (collectively, the "Information") are subject to change without notice.

KEMET K-SIM is designed to simulate behavior of components with respect to frequency, ambient temperature, and DC bias levels. The responses shown represent the typical response for each part type. Specific responses may vary, depending on manufacturing variation effects of all parameters involved, including the specified tolerances applied to capacitance and unspecified variations of ESR, ESL, and leakage resistance.

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If you have any questions please contact K-SIM.