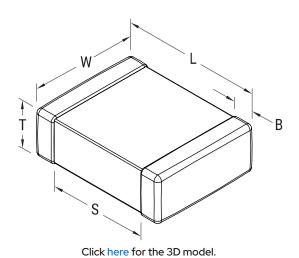


## C0603T100J5GACTU

**General Information** 

Aliases (C0603T100J5GAC7867) SMD COTS COG, Ceramic, 10 pF, 5%, 50 VDC, COG, SMD, MLCC, COTS, Ultra-Stable, Low Loss, Class I, 0603, 0.5 mm



Series	SMD COTS COG
Style	SMD Chip
Description	SMD, MLCC, COTS, Ultra-Stable, Low Loss, Class I
Features	Ultra-Stable, Low Loss, Class I
RoHS	Yes
Termination	Tin
Marking	No
Failure Rate	T I' NAU DDE EECOLDDA
railule Nate	Testing per MIL-PRF-55681 PDA 8%
AEC-Q200	
	8%
AEC-Q200	8% No

Dimensions	
Chip Size	0603
L	1.6mm +/-0.15mm
W	0.8mm +/-0.15mm
Т	0.8mm +/-0.07mm
S	0.5mm MIN
В	0.35mm +/-0.15mm

Specifications	
Capacitance	10 pF
Measurement Condition	1 MHz 1.0Vrms
Tolerance	5%
Voltage DC	50 VDC
Dielectric Withstanding Voltage	125 VDC
Temperature Range	-55/+125°C
Temp. Coefficient	COG
Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC)	30 ppm/C, 1MegaHz 1.0Vrms
Dissipation Factor	0.1% 1 MHz 1.0Vrms
Aging Rate	0% Loss/Decade Hour
Insulation Resistance	100 GOhms

Packaging Specifications	
Packaging	T&R, 180mm, Paper Tape
Packaging Quantity	4000

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