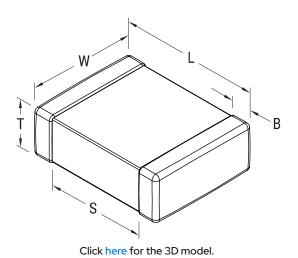


C0603T102K5RBCTU

General Information

Aliases (C0603T102K5RBC7867) SMD COTS X7R, Ceramic, 1,000 pF, 10%, 50 VDC, X7R, SMD, MLCC, COTS, Temperature Stable, Class II, 0603, 0.5 mm



Series	SMD COTS X7R	
Style	SMD Chip	
Description	SMD, MLCC, COTS, Temperature Stable, Class II	
Features	Temperature Stable, Class II	
RoHS	Yes	
Termination	Tin	
Marking	No	
Failure Rate	Testing per MIL-PRF-55681 PDA 8%, DPA per EIA-469	
AEC-Q200	No	
Typical Component Weight	4.8 mg	
Shelf Life	78 Weeks	
MSL	1	

Dimensions	
Chip Size	0603
L	1.6mm +/-0.15mm
W	0.8mm +/-0.15mm
Т	0.8mm +/-0.07mm
S	0.5mm MIN
В	0.35mm +/-0.15mm

Packaging Specifications	
В	0.35mm +/-0.15mm
S	0.5mm MIN
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Specifications		
Capacitance	1,000 pF	
Measurement Condition	1 kHz 1.0Vrms	
Tolerance	10%	
Voltage DC	50 VDC	
Dielectric Withstanding Voltage	125 VDC	
Temperature Range	-55/+125°C	
Temp. Coefficient	X7R	
Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC)	15%, 1kHz 1.0Vrms	
Dissipation Factor	2.5% 1 kHz 1.0Vrms	
Aging Rate	3% Loss/Decade Hour: Referee Time is 1000 Hours	
Insulation Resistance	100 GOhms	

gg - p		Capacitance Change with	15%, IKHZ I.UVrms
Packaging	T&R, 180mm, Paper Tape	Reference to +25°C and 0 VDC Applied (TCC)	
Packaging Quantity 4000	4000	Dissipation Factor	2.5% 1 kHz 1.0Vrms
		Aging Rate	3% Loss/Decade Hour: Referee Time is 1000 Hours
		Insulation Resistance	100 GOhms

Statements of suitability for certain applications are based on our knowledge of typical operating conditions for such applications, but are not intended to constitute - and we specifically disclaim - any warranty concerning suitability for a specific customer application or use. This Information is intended for use only by customers who have the requisite experience and capability to determine the correct products for their application. Any technical advice inferred from this Information or otherwise provided by us with reference to the use of our products is given gratis, and we assume no obligation or liability for the advice given or results obtained.

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