

C0805C105Z4VACTU

Aliases (C0805C105Z4VAC7800) SMD Comm Y5V, Ceramic, 1 uF, -20/+80%, 16 VDC, Y5V, SMD, MLCC, General Purpose, Class III, 0805, 0.7 mm



Click here for the 3D model.

General Information		
Series	SMD Comm Y5V	
Style	SMD Chip	
Description	SMD, MLCC, General Purpose, Class III	
Features	Class III	
RoHS	Yes	
Termination	Tin	
Marking	No	
AEC-Q200	No	
Typical Component Weight	13 mg	
Shelf Life	78 Weeks	
MSL	1	

	Specifications	
	Capacitance	1uF
	Measurement Condition	1 kHz 1.0Vrms
	Tolerance	-20/+80%
	Voltage DC	16 VDC
	Dielectric Withstanding Voltage	40 VDC
	Temperature Range	-30/+85°C
	Temp. Coefficient	Y5V
	Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC)	+22%/-82%, 1kHz 1.0Vrms
	Dissipation Factor	7% 1 kHz 1.0Vrms
	Aging Rate	7% Loss/Decade Hour: Referee Time is 48 Hours
	Insulation Resistance	100 MOhms

 Dimensions

 Chip Size
 0805

 L
 2mm +/-0.2mm

 W
 1.25mm +/-0.2mm

 T
 1.25mm +/-0.2mm

 S
 0.7mm MIN

 B
 0.5mm +/-0.25mm

Packaging Specifications

Packaging QuantityT&R, 180mm, Paper Tape2500

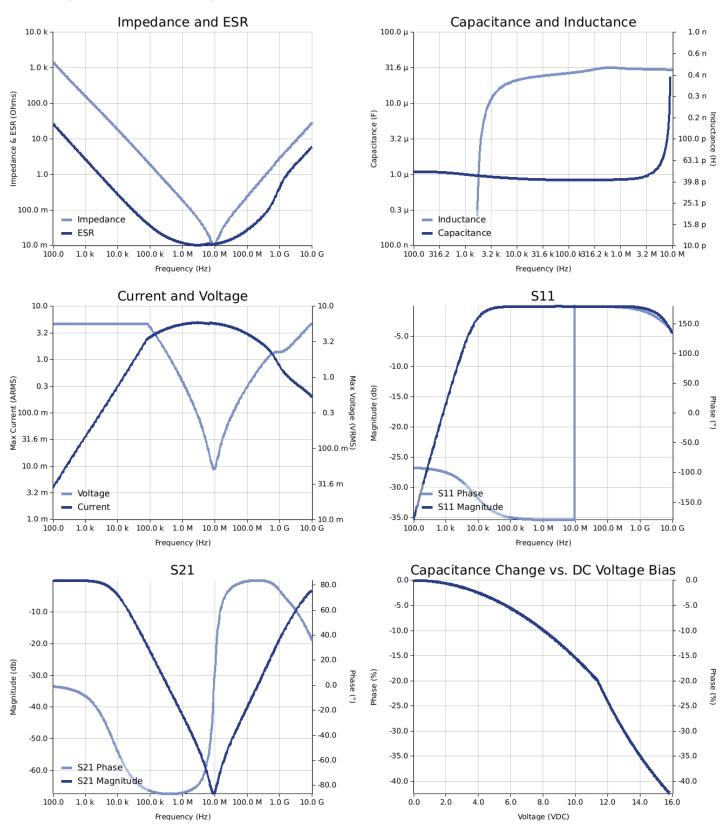
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Simulations

For the complete simulation environment please visit Y-SIM.





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These are simulations.

This is not a specification!

The responses shown represent the typical response for each part type. Specific responses may vary, depending on manufacturing variation affects of all parameters involved, including the specified tolerances applied to capacitance and unspecified variations of ESR, ESL, and leakage resistance.

The responses shown do not represent a specified or implied maximum capability of the device for all applications.

- The ESR used for ripple "Ripple Current/Voltage vs. Frequency" plots is the ESR at ambient temperature.

- The ESR is the "Temperature Rise vs. Ripple Current," voltage vs. Frequency plots is the ESR at ambient temperature. The ESR in the "Temperature Rise vs. Ripple Current," plots is adjusted to each incremental temperature rise before the power and ripple current is calculated. The effects shown herein are based on measured data from a multiple part sample of the parts in question. Ripple capability of this device will be factored by thermal resistance (Rth) created by circuit traces (addi affects of all parameters involved, including the specified tolerances applied to capacitance and unspecified variations of ESR, ESL, and leakage resistance. The peak voltages generated in the "Temperature Rise vs. Combined Ripple Currents" plot are calculated for each frequency and are not combined with voltages
- generated at any other harmonics. Please consult with the catalog or field applications engineer for maximum capability of the device in specific applications.

All product information and data (collectively, the "Information") are subject to change without notice.

KEMET K-SIM is designed to simulate behavior of components with respect to frequency, ambient temperature, and DC bias levels. The responses shown represent the typical response for each part type. Specific responses may vary, depending on manufacturing variation effects of all parameters involved, including the specified tolerances applied to capacitance and unspecified variations of ESR, ESL, and leakage resistance.

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If you have any questions please contact K-SIM.