

C0805C152J2GECTU

Aliases (C0805C152J2GEC7800)

Specifications

Insulation Resistance

ESD SMD Comm COG, Ceramic, 1,500 pF, 5%, 200 VDC, COG, SMD, MLCC, Temperature Stable, Electro Static Discharge, Class I, 0805, 0.7 mm



General Information	
Series	ESD SMD Comm COG
Style	SMD Chip
Description	SMD, MLCC, Temperature Stable, Electro Static Discharge, Class I
Features	Temperature Stable, Low ESR, Class I
RoHS	Yes
Termination	Tin
Marking	No
AEC-Q200	No
Typical Component Weight	11 mg
Shelf Life	78 Weeks
MSL	1

Dimensions	
Chip Size	0805
L	2mm +/-0.2mm
W	1.25mm +/-0.2mm
Т	0.78mm +/-0.10mm
S	0.7mm MIN
В	0.5mm +/-0.25mm

Capacitance	1,500 pF
Measurement Condition	1 kHz 1.0Vrms
Tolerance	5%
Voltage DC	200 VDC
ESD Level per AEC-Q200	8,000 V ESD Level
Dielectric Withstanding Voltage	500 VDC
Temperature Range	-55/+125°C
Temp. Coefficient	COG
Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC)	30 ppm/C, 1kHz 1.0Vrms
Dissipation Factor	0.1% 1 kHz 1.0Vrms
Aging Rate	0% Loss/Decade Hour

100 GOhms

Packaging Specifications	
Packaging	T&R, 180mm, Paper Tape
Packaging Quantity	4000

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