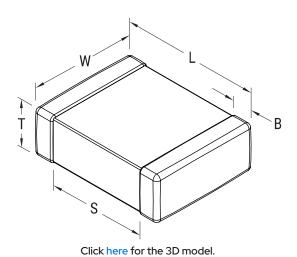


C1812J474K5RACTU

Specifications

Insulation Resistance

Aliases (C1812J474K5RAC7800) SMD Comm X7R FO, Ceramic, 0.47 uF, 10%, 50 VDC, X7R, SMD, MLCC, Open Mode, Temperature Stable, 1812, 2.3 mm



General Information	
Series	SMD Comm X7R FO
Style	SMD Chip
Description	SMD, MLCC, Open Mode, Temperature Stable
Features	Open Mode, Temperature Stable
RoHS	Yes
Termination	Flexible Termination
Marking	No
AEC-Q200	No
Typical Component Weight	95 mg
Shelf Life	78 Weeks
MSL	1

Dimensions	
Chip Size	1812
L	4.5mm +/-0.4mm
W	3.2mm +/-0.3mm
Т	1mm +/-0.10mm
S	2.3mm MIN
В	0.7mm +/-0.35mm

Capacitance	0.47 uF
Measurement Condition	1 kHz 1.0Vrms
Tolerance	10%
Voltage DC	50 VDC
Dielectric Withstanding Voltage	125 VDC
Temperature Range	-55/+125°C
Temp. Coefficient	X7R
Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC)	15%, 1kHz 1.0Vrms
Dissipation Factor	2.5% 1 kHz 1.0 Vrms
Aging Rate	3% Loss/Decade Hour: Referee Time is 1000 Hours

2.1277 GOhms

Packaging Specifications	
Packaging	T&R, 180mm, Plastic Tape
Packaging Quantity	1000

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