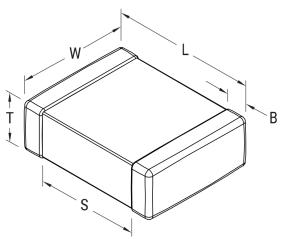


## C0603T101J5GALTU

Aliases (C0603T101J5GAL7867)

SMD COTS COG, Ceramic, 100 pF, 5%, 50 VDC, COG, SMD, MLCC, COTS, Ultra-Stable, Low Loss, Class I, 0603



Click here for the 3D model.

Dimensions	,
Chip Size	0603
L	1.6mm +/-0.15mm
W	0.8mm +/-0.15mm
Т	0.8mm +/-0.07mm
S	0.5mm MIN
В	0.35mm +/-0.15mm

Packaging Specifications	
Packaging	T&R, 180mm, Paper Tape
Packaging Quantity	4000

SMD COTS COG
SMD Chip
SMD, MLCC, COTS, Ultra-Stable, Low Loss, Class I
Ultra-Stable, Low Loss, Class I
No
▲ WARNING: Cancer and reproductive harm - http://www.p65warnings.ca.gov.
2d771165-5336-48a3-96fa-3663929fd828
Lead (SnPb)
false
Testing per MIL-PRF-55681 PDA 8%
No
3.7 mg
78 Weeks
1

Specifications	
Capacitance	100 pF
Measurement Condition	1 MHz 1.0Vrms
Capacitance Tolerance	5%
Voltage DC	50 VDC
Dielectric Withstanding Voltage	125 VDC
Temperature Range	-55/+125°C
Temperature Coefficient	COG
Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC)	30 ppm/C, 1MegaHz 1.0Vrms
Dissipation Factor	0.1% 1 MHz 1.0Vrms
Aging Rate	0% Loss/Decade Hour
Insulation Resistance	100 GOhms

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