

C0603T561J5RCCTU

Aliases (C0603T561J5RCC7867)

SMD COTS X7R, Ceramic, 560 pF, 5%, 50 VDC, X7R, SMD, MLCC, COTS, Temperature Stable, Class II, 0603



Click [here](#) for the 3D model.

General Information

Series	SMD COTS X7R
Style	SMD Chip
Description	SMD, MLCC, COTS, Temperature Stable, Class II
Features	Temperature Stable, Class II
RoHS	Yes
Termination	Tin
Marking	false
Failure Rate	Testing per MIL-PRF-55681 PDA 8%, DPA per EIA-469, Humidity per MIL-STD-202, Method 103, Condition A
AEC-Q200	No
Typical Component Weight	4.8 mg
Shelf Life	78 Weeks
MSL	1

Specifications

Capacitance	560 pF
Measurement Condition	1 kHz 1.0Vrms
Capacitance Tolerance	5%
Voltage DC	50 VDC
Dielectric Withstanding Voltage	125 VDC
Temperature Range	-55/+125°C
Temperature Coefficient	X7R
Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC)	15%, 1kHz 1.0Vrms
Dissipation Factor	2.5% 1kHz 1.0Vrms
Aging Rate	3% Loss/Decade Hour: Referee Time is 1000 Hours
Insulation Resistance	100 GOhms

Dimensions

Chip Size	0603
L	1.6mm +/-0.15mm
W	0.8mm +/-0.15mm
T	0.8mm +/-0.07mm
S	0.5mm MIN
B	0.35mm +/-0.15mm

Packaging Specifications

Packaging	T&R, 180mm, Paper Tape
Packaging Quantity	4000

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