

C0805C473J3GECTU

Aliases (C0805C473J3GEC7800) ESD SMD Comm C0G, Ceramic, 0.047 uF, 5%, 25 VDC, C0G, SMD, MLCC, Temperature Stable, Electro Static Discharge, Class I, 0805, 0.7 mm



General Information	
Series	ESD SMD Comm COG
Style	SMD Chip
Description	SMD, MLCC, Temperature Stable, Electro Static Discharge, Class I
Features	Temperature Stable, Low ESR, Class I
RoHS	Yes
Termination	Tin
Marking	No
AEC-Q200	No
Typical Component Weight	14 mg
Shelf Life	78 Weeks
MSL	1

Dimensions	
Chip Size	0805
L	2mm +/-0.2mm
W	1.25mm +/-0.2mm
Т	1.25mm +/-0.15mm
S	0.7mm MIN
В	0.5mm +/-0.25mm

В	0.5mm +/-0.25mm
Packaging Specifications	
Packaging	T&R, 180mm, Plastic Tape

2500

Packaging Quantity

Specifications	
Capacitance	0.047 uF
Measurement Condition	1 kHz 1.0Vrms
Tolerance	5%
Voltage DC	25 VDC
ESD Level per AEC-Q200	25,000 V ESD Level
Dielectric Withstanding Voltage	62.5 VDC
Temperature Range	-55/+125°C
Temp. Coefficient	COG
Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC)	30 ppm/C, 1kHz 1.0Vrms
Dissipation Factor	0.1% 1 kHz 1.0Vrms
Aging Rate	0% Loss/Decade Hour
Insulation Resistance	21.2766 GOhms

Statements of suitability for certain applications are based on our knowledge of typical operating conditions for such applications, but are not intended to constitute - ar	nd
we specifically disclaim - any warranty concerning suitability for a specific customer application or use. This Information is intended for use only by customers who have t	the
requisite experience and capability to determine the correct products for their application. Any technical advice inferred from this Information or otherwise provided by	us
with reference to the use of our products is given gratis, and we assume no obligation or liability for the advice given or results obtained.	

Generated 08/29/2025 © 2006 - 2025 YAGEO