



ESD SMD Comm COG, Ceramic, 0.068 uF, 10%, 50 VDC, COG, SMD, MLCC, Temperature Stable, Electro Static Discharge, Class I, 1206, 1.5 mm



General Information		
Series	ESD SMD Comm COG	
Style	SMD Chip	
Description	SMD, MLCC, Temperature Stable, Electro Static Discharge, Class I	
Features	Temperature Stable, Low ESR, Class I	
RoHS	Yes	
Termination	Tin	
Marking	No	
AEC-Q200	No	
Typical Component Weight	36 mg	
Shelf Life	78 Weeks	
MSL	1	

Dimensions	
Chip Size	1206
L	3.2mm +/-0.2mm
W	1.6mm +/-0.2mm
Т	1.6mm +/-0.20mm
S	1.5mm MIN
В	0.5mm +/-0.25mm

Т	1.6mm +/-0.20mm
S	1.5mm MIN
В	0.5mm +/-0.25mm
Packaging Specifications	

Specifications	
Capacitance	0.068 uF
Measurement Condition	1 kHz 1.0Vrms
Tolerance	10%
Voltage DC	50 VDC
ESD Level per AEC-Q200	25,000 V ESD Level
Dielectric Withstanding Voltage	125 VDC
Temperature Range	-55/+125°C
Temp. Coefficient	COG
Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC)	30 ppm/C, 1kHz 1.0Vrms
Dissipation Factor	0.1% 1 kHz 1.0Vrms
Aging Rate	0% Loss/Decade Hour
Insulation Resistance	14.7059 GOhms

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Packaging	Bulk, Bag	Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC)	30 ppm/C, 1kHz 1.0Vrms
Packaging Quantity 1	1		
		Dissipation Factor	0.1% 1 kHz 1.0Vrms
		Aging Rate	0% Loss/Decade Hour

Statements of suitability for certain applications are based on our knowledge of typical operating conditions for such applications, but are not intended to constitute - and we specifically disclaim - any warranty concerning suitability for a specific customer application or use. This Information is intended for use only by customers who have the requisite experience and capability to determine the correct products for their application. Any technical advice inferred from this Information or otherwise provided by us with reference to the use of our products is given gratis, and we assume no obligation or liability for the advice given or results obtained.

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