

## C1210X103KFRACTU

Aliases (C1210X103KFRAC7800) SMD Comm X7R HV Flex, Ceramic, 0.01 uF, 10%, 1500 VDC, X7R, SMD, MLCC, FT-CAP, Temperature Stable, 1210



Click here for the 3D model.

General Information		
Series	SMD Comm X7R HV Flex	
Style	SMD Chip	
Description	SMD, MLCC, FT-CAP, Temperature Stable	
Features	FT-CAP, Temperature Stable	
RoHS	Yes	
Termination	Flexible Termination	
Marking	false	
AEC-Q200	No	
Typical Component Weight	105 mg	
Shelf Life	78 Weeks	
MSL	1	

100 GOhms

	Specifications	
1210	Capacitance	0.01 uF
3.3mm +/-0.4mm	Measurement Condition	1 kHz 1.0Vrms
2.6mm +/-0.3mm	Capacitance Tolerance	10%
2.1mm +/-0.20mm	Voltage DC	1500 VDC
1.5mm MIN	Dielectric Withstanding Voltage	1800 VDC
0.6mm +/-0.25mm	Temperature Range	-55/+125°C
	Temperature Coefficient	X7R
	Capacitance Change with	15%, 1kHz 1.0Vrms
T&R, 180mm, Plastic Tape	Reference to +25°C and 0 VDC Applied (TCC)	
2000	Dissipation Factor	2.5% 1 kHz 1.0Vrms
	Aging Rate	3% Loss/Decade Hour: Referee Time is 1000 Hours

Insulation Resistance

**Packaging Specifications** 

T&R, 180mr Packaging Packaging Quantity 2000

Statements of suitability for certain applications are based on our knowledge of typical operating conditions for such applications, but are not intended to constitute - and we specifically disclaim - any warranty concerning suitability for a specific customer application or use. This Information is intended for use only by customers who have the requisite experience and capability to determine the correct products for their application. Any technical advice inferred from this Information or otherwise provided by us with reference to the use of our products is given gratis, and we assume no obligation or liability for the advice given or results obtained.

Dimensions

Chip Size

L W

Т

s

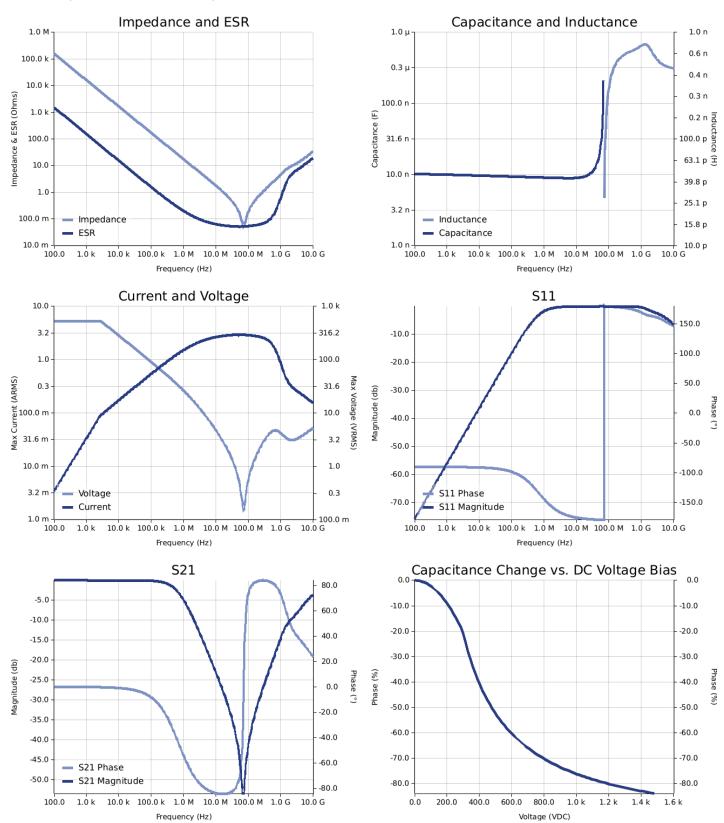
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## Simulations

For the complete simulation environment please visit K-SIM.





MLCC, FT-CAP, Temperature Stable, 1210

## These are simulations.

This is not a specification!

The responses shown represent the typical response for each part type. Specific responses may vary, depending on manufacturing variation affects of all parameters involved, including the specified tolerances applied to capacitance and unspecified variations of ESR, ESL, and leakage resistance.

The responses shown do not represent a specified or implied maximum capability of the device for all applications.

- The ESR used for ripple "Ripple Current/Voltage vs. Frequency" plots is the ESR at ambient temperature.

- The ESR used for hipple klipple current/ voltage vs. requericy plots is the ESR at an originatine.
  The ESR in the "Temperature Rise vs. Ripple Current" plots is adjusted to each incremental temperature rise before the power and ripple current is calculated.
  The effects shown herein are based on measured data from a multiple part sample of the parts in question.
  Ripple capability of this device will be factored by thermal resistance (Rth) created by circuit traces (addi affects of all parameters involved, including the specified tolerances applied to capacitance and unspecified variations of ESR, ESL, and leakage resistance.
  The peak voltages generated in the "Temperature Rise vs. Combined Ripple Currents" plot are calculated for each frequency and are not combined with voltages are applied to reach previous the burger of the parts.
- generated at any other harmonics. Please consult with the catalog or field applications engineer for maximum capability of the device in specific applications.

All product information and data (collectively, the "Information") are subject to change without notice.

KEMET K-SIM is designed to simulate behavior of components with respect to frequency, ambient temperature, and DC bias levels. The responses shown represent the typical response for each part type. Specific responses may vary, depending on manufacturing variation effects of all parameters involved, including the specified tolerances applied to capacitance and unspecified variations of ESR, ESL, and leakage resistance.

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If you have any questions please contact K-SIM.