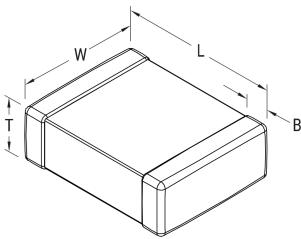


C1825T105K2RBL7210

SMD COTS X7R, Ceramic, 1 uF, 10%, 200 VDC, X7R, SMD, MLCC, COTS, Temperature Stable, Class II, 1825



Click here for the 3D model.

Dimensions	
Chip Size	1825
L	4.5mm +/-0.3mm
W	6.4mm +/-0.4mm
Т	1.5mm +/-0.15mm
В	0.6mm +/-0.35mm

Packaging Specifications	,
Packaging	T&R, 330mm, Plastic Tape
Packaging Quantity	4000

General Information	
Series	SMD COTS X7R
Style	SMD Chip
Description	SMD, MLCC, COTS, Temperature Stable, Class II
Features	Temperature Stable, Class II
RoHS	No
Prop 65	▲ WARNING: Cancer and reproductive harm - http://www.p65warnings.ca.gov.
Termination	Lead (SnPb)
Marking	false
Failure Rate	Testing per MIL-PRF-55681 PDA 8%, DPA per EIA-469
AEC-Q200	No
Typical Component Weight	240 mg
Shelf Life	78 Weeks
MSL	1

Specifications	
Capacitance	1uF
Measurement Condition	1 kHz 1.0Vrms
Capacitance Tolerance	10%
Voltage DC	200 VDC
Dielectric Withstanding Voltage	500 VDC
Temperature Range	-55/+125°C
Temperature Coefficient	X7R
Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC)	15%, 1kHz 1.0Vrms
Dissipation Factor	2.5% 1 kHz 1.0Vrms
Aging Rate	3% Loss/Decade Hour: Referee Time is 1000 Hours
Insulation Resistance	1GOhms

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