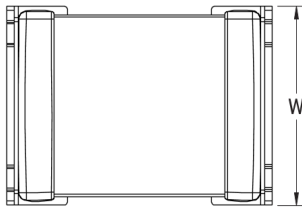


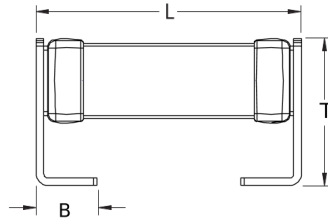
TOP VIEW

Single or Double Chip Stack



PROFILE VIEW

Single Chip Stack



Click [here](#) for the 3D model.

Dimensions

L	6mm +/-0.5mm
W	5mm +/-0.5mm
T	3.5mm +/-0.30mm
B	1.6mm +/-0.3mm

Packaging Specifications

Packaging	T&R, 180mm, Plastic Tape
Packaging Quantity	300

General Information

Series	KPS SMD Comm X7R HV
Style	Stacked Chip
Description	SMD, MLCC, Stacked, Single Chip, High Voltage, Temperature Stable
Features	Temperature Stable
RoHS	Yes
Termination	Tin
AEC-Q200	No
Typical Component Weight	410 mg
Miscellaneous	X7R dielectric is not recommended for AC line filtering or pulse applications.
Chip Size	2220-1
Shelf Life	78 Weeks
MSL	1

Specifications

Capacitance	0.1 uF
Measurement Condition	1 kHz 1.0Vrms
Capacitance Tolerance	20%
Voltage DC	630 VDC
Dielectric Withstanding Voltage	945 VDC
Temperature Range	-55/+125°C
Temperature Coefficient	X7R
Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC)	15%, 1kHz 1.0Vrms
Dissipation Factor	2.5% 1kHz 1.0Vrms
Aging Rate	3% Loss/Decade Hour: Referee Time is 1000 Hours
Insulation Resistance	10 GOhms

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