

T491S685M004AT

General Information

Series

Style

RoHS

Dielectric

Description

Termination

AEC-Q200

T491, Tantalum, MnO2 Tantalum, 6.8 uF, 20%, 4 VDC, SMD, MnO2, Molded, Low Profile, 15 Ohms, 3216, 1.2mm

T491

Yes

Tin

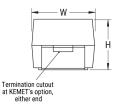
No

MnO2 Tantalum

SMD, MnO2, Molded, Low Profile

SMD Chip

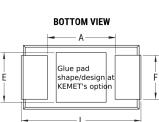
CATHODE (-) END VIEW



SIDE VIEW

ANODE (+) END VIEW





Click here for the 3D model.

Dimensions	
Footprint	3216
L	3.2mm +/-0.2mm
W	1.6mm +/-0.2mm
Н	1.1mm +/-0.1mm
Т	0.13mm REF
S	0.8mm +0.2/-0.3mm
F	1.2mm +/-0.1mm
А	1.2mm MIN
E	1.3mm REF
G	1.1mm REF
Х	0.1mm REF

Т

Typical Component Weight		41.42 mg
Shelf Life		156 Weeks
MSL		1
Specifications		
Capacitance	6.8 uF	
Capacitance Tolerance	20%	
Voltage DC	4 VDC (85C), 2.68 VDC (125C)	
Temperature Range	-55/+125	°C

Capacitance	0.0 ui
Capacitance Tolerance	20%
Voltage DC	4 VDC (85C), 2.68 VDC (125C)
Temperature Range	-55/+125°C
Rated Temperature	85°C
Dissipation Factor	6% 120Hz 25C
Failure Rate	N/A
ESR	15 Ohms (100kHz 25C)
Ripple Current	63 mA (rms, 100kHz 25C), 56.7 mA (rms, 85C), 25.2 mA (rms, 125C)
Leakage Current	0.5 uA (5min 25°C)

Packaging Specifications		
Packaging	T&R, 178mm	
Packaging Quantity	2500	

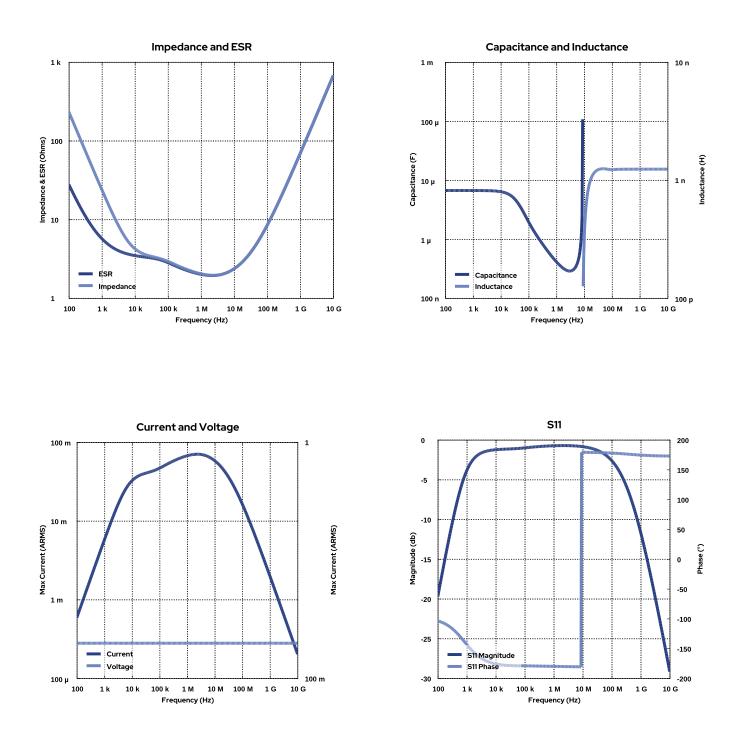
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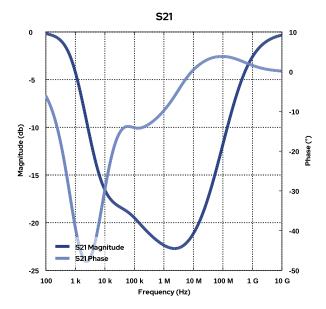
Simulations

For the complete simulation environment please visit K-SIM.





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These are simulations.

This is not a specification!

The responses shown represent the typical response for each part type. Specific responses may vary, depending on manufacturing variation affects of all parameters involved, including the specified tolerances applied to capacitance and unspecified variations of ESR, ESL, and leakage resistance.

The responses shown do not represent a specified or implied maximum capability of the device for all applications.

- The ESR used for ripple "Ripple Current/Voltage vs. Frequency" plots is the ESR at ambient temperature.
- The ESR in the "Temperature Rise vs. Ripple Current" plots is adjusted to each incremental temperature rise before the power and ripple current is calculated.
- The effects shown herein are based on measured data from a multiple part sample of the parts in question.
- Ripple capability of this device will be factored by thermal resistance (Rth) created by circuit traces (addi affects of all parameters involved, including the specified tolerances applied to capacitance and unspecified variations of ESR, ESL, and leakage resistance. The peak voltages generated in the "Temperature Rise vs. Combined Ripple Currents" plot are calculated for each frequency and are not combined with voltages generated at any other
- harmonics.
- Please consult with the catalog or field applications engineer for maximum capability of the device in specific applications.

All product information and data (collectively, the "Information") are subject to change without notice.

KEMET K-SIM is designed to simulate behavior of components with respect to frequency, ambient temperature, and DC bias levels. The responses shown represent the typical response for each part type. Specific responses may vary, depending on manufacturing variation effects of all parameters involved, including the specified tolerances applied to capacitance and unspecified variations of ESR, ESL, and leakage resistance.

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If you have any questions please contact K-SIM.