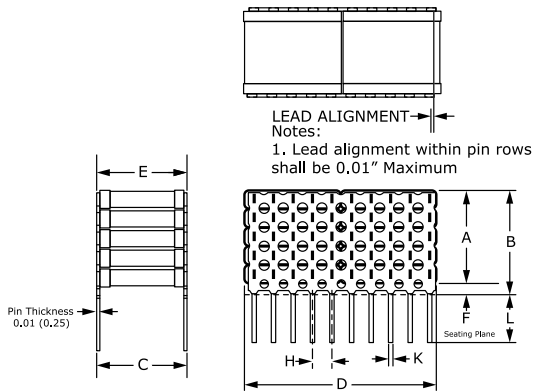


KPS-MCC Indust COG HT200C, Ceramic, 200C, 2 uF, 10%, 500 V, COG



| Dimensions | |
|------------|--------------------|
| D | 25.64mm +/-0.635mm |
| L | 6.35mm MIN |
| H | 2.54mm NOM |
| F | 1.397mm +/-0.25mm |
| A | 13.46mm MAX |
| B | 15.238mm MAX |
| C | 11.43mm +/-0.635mm |
| E | 12.7mm MAX |
| K | 0.5mm NOM |

| Packaging Specifications | |
|--------------------------|----|
| Packaging Quantity: | 16 |

| General Information | |
|---------------------|--|
| Supplier: | KEMET |
| Series: | KPS-MCC Indust COG HT200C |
| Style: | Leaded Stacked Chip |
| Description: | Low ESR, Stacked Ceramic Chips |
| Features: | 200C, Low ESR, High Thermal Stability, Bulk Capacitance |
| RoHS: | With Exemptions |
| Termination: | Silver |
| Lead: | Straight Leads |
| Notes: | Note: Number Of Chips In Stack Depends On Design. Number Of Chips In This Stack = 10 |

| Specifications | |
|----------------------------------|---------------------|
| Capacitance: | 2 uF |
| Capacitance Tolerance: | 10% |
| Voltage DC: | 500 VDC |
| Dielectric Withstanding Voltage: | 650 V |
| Temperature Range: | -55/+200C |
| Temperature Coefficient: | COG |
| Dissipation Factor: | 0.10% 1kHz 25C |
| Aging Rate: | 0% Loss/Decade Hour |
| Insulation Resistance: | 500 MOhms |