

C0603C102J3GECTU

Aliases (C0603C102J3GEC7867) ESD SMD Comm C0G, Ceramic, 1,000 pF, 5%, 25 VDC, C0G, SMD, MLCC, Temperature Stable, Electro Static Discharge, Class I, 0603, 0.5 mm



Click here for the 3D model.

General Information	
Series	ESD SMD Comm COG
Style	SMD Chip
Description	SMD, MLCC, Temperature Stable, Electro Static Discharge, Class I
Features	Temperature Stable, Low ESR, Class I
RoHS	Yes
Termination	Tin
Marking	No
AEC-Q200	No
Typical Component Weight	3.7 mg
Shelf Life	78 Weeks
MSL	1

Dimensions	
Chip Size	0603
L	1.6mm +/-0.15mm
W	0.8mm +/-0.15mm
т	0.8mm +/-0.07mm
S	0.5mm MIN
В	0.35mm +/-0.15mm

Packaging Specifications

Packaging	T&R, 180mm, Paper Tape
Packaging Quantity	4000

Specifications	
Capacitance	1,000 pF
Measurement Condition	1 MHz 1.0Vrms
Tolerance	5%
Voltage DC	25 VDC
ESD Level per AEC-Q200	6,000 V ESD Level
Dielectric Withstanding Voltage	62.5 VDC
Temperature Range	-55/+125°C
Temp. Coefficient	COG
Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC)	30 ppm/C, 1MegaHz 1.0Vrms
Dissipation Factor	0.1% 1 MHz 1.0Vrms
Aging Rate	0% Loss/Decade Hour
Insulation Resistance	100 GOhms

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