

## C0603C154M3VACTU

Aliases (C0603C154M3VAC7867) SMD Comm Y5V, Ceramic, 0.15 uF, 20%, 25 VDC, Y5V, SMD, MLCC, General Purpose, Class III, 0603, 0.5 mm



General Information		
Series	SMD Comm Y5V	
Style	SMD Chip	
Description	SMD, MLCC, General Purpose, Class III	
Features	Class III	
RoHS	Yes	
Termination	Tin	
Marking	No	
AEC-Q200	No	
Typical Component Weight	6.5 mg	
Shelf Life	78 Weeks	
MSL	1	

	Specifications	
0603	Capacitance	0.15 uF
1.6mm +/-0.15mm	Measurement Condition	1 kHz 1.0Vrms
0.8mm +/-0.15mm	Tolerance	20%
0.8mm +/-0.10mm	Voltage DC	25 VDC
0.5mm MIN	Dielectric Withstanding Voltage	62.5 VDC
0.35mm +/-0.15mm	Temperature Range	-30/+85°C
	Temp. Coefficient	Y5V
	Capacitance Change with	+22%/-82%, 1kHz 1.0Vrms
T&R, 180mm, Paper Tape	Reference to +25°C and 0 VDC Applied (TCC)	
4000	Dissipation Factor	7%1kHz1.0Vrms

Aging Rate

Insulation Resistance

Click here for the 3D model.

Dimensions	
Chip Size	0603
L	1.6mm +/-0.15mm
W	0.8mm +/-0.15mm
Т	0.8mm +/-0.10mm
S	0.5mm MIN
В	0.35mm +/-0.15mm

## **Packaging Specifications**

Packaging	T&R, 180mm, Paper Tape
Packaging Quantity	4000

7% Loss/Decade Hour: Referee

Time is 1000 Hours

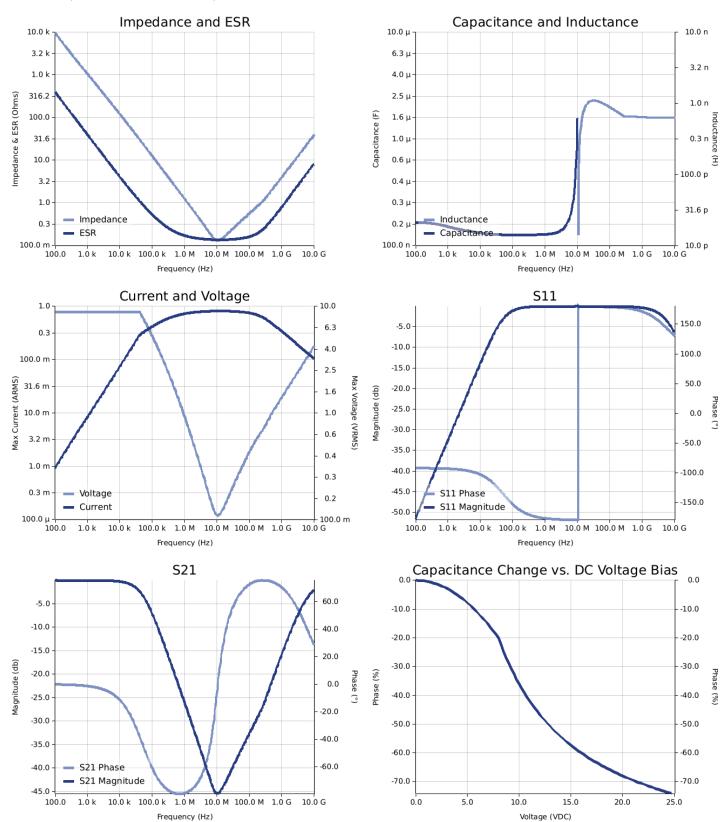
666.7 MOhms



CO603C154M3VACTU Aliases (C0603C154M3VAC7867) SMD Comm Y5V, Ceramic, 0.15 uF, 20%, 25 VDC, Y5V, SMD, MLCC, General Purpose, Class III, 0603, 0.5 mm

## Simulations

For the complete simulation environment please visit K-SIM.





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## These are simulations.

This is not a specification!

The responses shown represent the typical response for each part type. Specific responses may vary, depending on manufacturing variation affects of all parameters involved, including the specified tolerances applied to capacitance and unspecified variations of ESR, ESL, and leakage resistance.

The responses shown do not represent a specified or implied maximum capability of the device for all applications.

- The ESR used for ripple "Ripple Current/Voltage vs. Frequency" plots is the ESR at ambient temperature.

- The ESR used for hipple klipple current younge vs. requericy plots is the ESR at an bient temperature.
  The ESR in the "Temperature Rise vs. Ripple Current" plots is adjusted to each incremental temperature rise before the power and ripple current is calculated.
  The effects shown herein are based on measured data from a multiple part sample of the parts in question.
  Ripple capability of this device will be factored by thermal resistance (Rth) created by circuit traces (addi affects of all parameters involved, including the specified tolerances applied to capacitance and unspecified variations of ESR, ESL, and leakage resistance.
  The peak voltages generated in the "Temperature Rise vs. Combined Ripple Currents" plot are calculated for each frequency and are not combined with voltages are applied to reach previous the burger of the parts.
- generated at any other harmonics. Please consult with the catalog or field applications engineer for maximum capability of the device in specific applications.

All product information and data (collectively, the "Information") are subject to change without notice.

KEMET K-SIM is designed to simulate behavior of components with respect to frequency, ambient temperature, and DC bias levels. The responses shown represent the typical response for each part type. Specific responses may vary, depending on manufacturing variation effects of all parameters involved, including the specified tolerances applied to capacitance and unspecified variations of ESR, ESL, and leakage resistance.

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If you have any questions please contact K-SIM.