

## C0603T182JAGCLTU

Aliases (C0603T182JAGCL7867) SMD COTS COG, Ceramic, 1,800 pF, 5%, 250 VDC, COG, SMD, MLCC, COTS, Ultra-Stable, Low Loss, Class I, 0603, 0.5 mm



Click here for the 3D model.

SMD COTS COG SMD Chip
SMD Chip
MD, MLCC, COTS, Ultra-Stable, Low Loss, Class I
Jltra-Stable, Low Loss, Class I
ło
VARNING: Cancer and eproductive harm - https://www.p65warnings.ca.gov
2d771165-5336-48a3-96fa-366 3929fd828
.ead (SnPb)
ło
Testing per MIL-PRF-55681 PDA 3%, DPA per EIA-469, Humidity per MIL-STD-202, Method 103, Condition A
lo
1.6 mg
78 Weeks

Dimensions	
Chip Size	0603
L	1.6mm +/-0.15mm
W	0.8mm +/-0.15mm
т	0.85mm +/-0.07mm
S	0.5mm MIN
В	0.35mm +/-0.15mm

Packaging Specifications	
Packaging	T&R, 180mm, Paper Tape
Packaging Quantity	4000

Specifications	
Capacitance	1,800 pF
Measurement Condition	1 kHz 1.0Vrms
Tolerance	5%
Voltage DC	250 VDC
Dielectric Withstanding Voltage	625 VDC
Temperature Range	-55/+125°C
Temp. Coefficient	COG
Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC)	30 ppm/C, 1kHz 1.0Vrms
Dissipation Factor	0.1% 1 kHz 1.0Vrms
Aging Rate	0% Loss/Decade Hour
Insulation Resistance	100 GOhms

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