

## C0603T750J1GCLTU

Aliases (C0603T750J1GCL7867)

SMD COTS COG, Ceramic, 75 pF, 5%, 100 VDC, COG, SMD, MLCC, COTS, Ultra-Stable, Low Loss, Class I, 0603, 0.5 mm



Click [here](#) for the 3D model.

### General Information

|                          |                                                                                                                              |
|--------------------------|------------------------------------------------------------------------------------------------------------------------------|
| Series                   | SMD COTS COG                                                                                                                 |
| Style                    | SMD Chip                                                                                                                     |
| Description              | SMD, MLCC, COTS, Ultra-Stable, Low Loss, Class I                                                                             |
| Features                 | Ultra-Stable, Low Loss, Class I                                                                                              |
| RoHS                     | No                                                                                                                           |
| Prop 65                  | <b>WARNING:</b> Cancer and reproductive harm - <a href="https://www.p65warnings.ca.gov/">https://www.p65warnings.ca.gov/</a> |
| SCIP Number              | 2d771165-5336-48a3-96fa-3663929fd828                                                                                         |
| Termination              | Lead (SnPb)                                                                                                                  |
| Marking                  | No                                                                                                                           |
| Failure Rate             | Testing per MIL-PRF-55681 PDA 8%, DPA per EIA-469, Humidity per MIL-STD-202, Method 103, Condition A                         |
| AEC-Q200                 | No                                                                                                                           |
| Typical Component Weight | 3.7 mg                                                                                                                       |
| Shelf Life               | 78 Weeks                                                                                                                     |
| MSL                      | 1                                                                                                                            |

### Dimensions

|           |                  |
|-----------|------------------|
| Chip Size | 0603             |
| L         | 1.6mm +/-0.15mm  |
| W         | 0.8mm +/-0.15mm  |
| T         | 0.8mm +/-0.07mm  |
| S         | 0.5mm MIN        |
| B         | 0.35mm +/-0.15mm |

### Packaging Specifications

|                    |                        |
|--------------------|------------------------|
| Packaging          | T&R, 180mm, Paper Tape |
| Packaging Quantity | 4000                   |

### Specifications

|                                                                    |                        |
|--------------------------------------------------------------------|------------------------|
| Capacitance                                                        | 75 pF                  |
| Measurement Condition                                              | 1 MHz 1.0Vrms          |
| Tolerance                                                          | 5%                     |
| Voltage DC                                                         | 100 VDC                |
| Dielectric Withstanding Voltage                                    | 250 VDC                |
| Temperature Range                                                  | -55/+125°C             |
| Temp. Coefficient                                                  | COG                    |
| Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC) | 30 ppm/C, 1MHz 1.0Vrms |
| Dissipation Factor                                                 | 0.1% 1 MHz 1.0Vrms     |
| Aging Rate                                                         | 0% Loss/Decade Hour    |
| Insulation Resistance                                              | 100 GOhms              |

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