

## C0805T103J3GCLTU

Aliases (C0805T103J3GCL7800)

SMD COTS COG, Ceramic, 0.01 uF, 5%, 25 VDC, COG, SMD, MLCC, COTS, Ultra-Stable, Low Loss, Class I, 0805, 0.7 mm



Click [here](#) for the 3D model.

### General Information

Series	SMD COTS COG
Style	SMD Chip
Description	SMD, MLCC, COTS, Ultra-Stable, Low Loss, Class I
Features	Ultra-Stable, Low Loss, Class I
RoHS	No
Prop 65	<b>WARNING:</b> Cancer and reproductive harm - <a href="https://www.p65warnings.ca.gov/">https://www.p65warnings.ca.gov/</a>
SCIP Number	2d771165-5336-48a3-96fa-3663929fd828
Termination	Lead (SnPb)
Marking	No
Failure Rate	Testing per MIL-PRF-55681 PDA 8%, DPA per EIA-469, Humidity per MIL-STD-202, Method 103, Condition A
AEC-Q200	No
Typical Component Weight	11 mg
Shelf Life	78 Weeks
MSL	1

### Dimensions

Chip Size	0805
L	2mm +/-0.2mm
W	1.25mm +/-0.2mm
T	0.78mm +/-0.10mm
S	0.7mm MIN
B	0.5mm +/-0.25mm

### Packaging Specifications

Packaging	T&R, 180mm, Paper Tape
Packaging Quantity	4000

### Specifications

Capacitance	0.01 uF
Measurement Condition	1 kHz 1.0Vrms
Tolerance	5%
Voltage DC	25 VDC
Dielectric Withstanding Voltage	62.5 VDC
Temperature Range	-55/+125°C
Temp. Coefficient	COG
Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC)	30 ppm/C, 1kHz 1.0Vrms
Dissipation Factor	0.1% 1 kHz 1.0Vrms
Aging Rate	0% Loss/Decade Hour
Insulation Resistance	100 GOhms

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