

## C0805T330J5GCLTU

Aliases (C0805T330J5GCL7800) SMD COTS COG, Ceramic, 33 pF, 5%, 50 VDC, COG, SMD, MLCC, COTS, Ultra-Stable, Low Loss, Class I, 0805, 0.7 mm



General Information		
Series	SMD COTS COG	
Style	SMD Chip	
Description	SMD, MLCC, COTS, Ultra-Stable, Low Loss, Class I	
Features	Ultra-Stable, Low Loss, Class I	
RoHS	No	
Prop 65	WARNING: Cancer and reproductive harm - https://www.p65warnings.ca.gov /	
SCIP Number	2d771165-5336-48a3-96fa-366 3929fd828	
Termination	Lead (SnPb)	
Marking	No	
Failure Rate	Testing per MIL-PRF-55681 PDA 8%, DPA per EIA-469, Humidity per MIL-STD-202, Method 103, Condition A	
AEC-Q200	No	
Typical Component Weight	11 mg	
Shelf Life	78 Weeks	
MSL	1	

33 pF

1 MHz 1.0Vrms

100 GOhms

Dimensions	
Chip Size	0805
L	2mm +/-0.2mm
W	1.25mm +/-0.2mm
Т	0.78mm +/-0.10mm
S	0.7mm MIN
В	0.5mm +/-0.25mm

W	1.25mm +/-0.2mm	Tolerance	5%
Т	0.78mm +/-0.10mm	Voltage DC	50 VDC
S	0.7mm MIN	Dielectric Withstanding Voltage	125 VDC
В	0.5mm +/-0.25mm	Temperature Range	-55/+125°C
		Temp. Coefficient	COG
Packaging Specifications		Capacitance Change with	30 ppm/C, 1MegaHz 1.0Vrms
Packaging	T&R, 180mm, Paper Tape	Reference to +25°C and 0 VDC Applied (TCC)	.,,,
Packaging Quantity	uantity 4000	Dissipation Factor	0.1% 1 MHz 1.0Vrms
	Aging Rate	0% Loss/Decade Hour	

**Specifications** Capacitance

Measurement Condition

Insulation Resistance

Statements of suitability for certain applications are based on our knowledge of typical operating conditions for such applications, but are not intended to constitute - and we specifically disclaim - any warranty concerning suitability for a specific customer application or use. This Information is intended for use only by customers who have the requisite experience and capability to determine the correct products for their application. Any technical advice inferred from this Information or otherwise provided by us with reference to the use of our products is given gratis, and we assume no obligation or liability for the advice given or results obtained.

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