

C1206C474KMRECTU

Aliases (C1206C474KMREC7800)

General Information

Series

Style Description

Features

ESD SMD Comm X7R, Ceramic, 0.47 uF, 10%, 63 VDC, X7R, SMD, MLCC, Temperature Stable, Electro Static Discharge, Class II, 1206, 1.5 mm

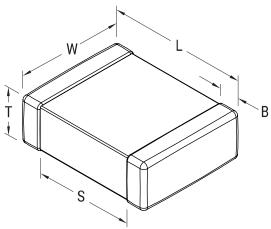
ESD SMD Comm X7R

SMD, MLCC, Temperature Stable, Electro Static Discharge,

Temperature Stable, Class II

SMD Chip

Class II



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			RoHS	Yes
Click here for the 3D model.		J	Termination	Tin
			Marking	No
			AEC-Q200	No
			Typical Component Weight	41 mg
			Shelf Life	78 Week
			MSL	1
Dimensions			Specifications	
Chip Size	1206		Capacitance	0.47 uF
L	3.2mm +/-0.2mm		Measurement Condition	1 kHz 1.0\

В	0.5mm +/-0.25mm		
Packaging Specifications			
Packaging	T&R, 180mm, Plastic Tape		
Packaging Quantity	2000		

1.6mm +/-0.2mm 1.6mm +/-0.20mm

1.5mm MIN

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Specifications	
Capacitance	0.47 uF
Measurement Condition	1 kHz 1.0Vrms
Tolerance	10%
Voltage DC	63 VDC
ESD Level per AEC-Q200	25,000 V ESD Level
Dielectric Withstanding Voltage	157.5 VDC
Temperature Range	-55/+125°C
Temp. Coefficient	X7R
Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC)	15%, 1kHz 1.0Vrms
Dissipation Factor	2.5% 1 kHz 1.0Vrms
Aging Rate	3% Loss/Decade Hour: Referee Time is 1000 Hours
Insulation Resistance	1.0638 GOhms

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