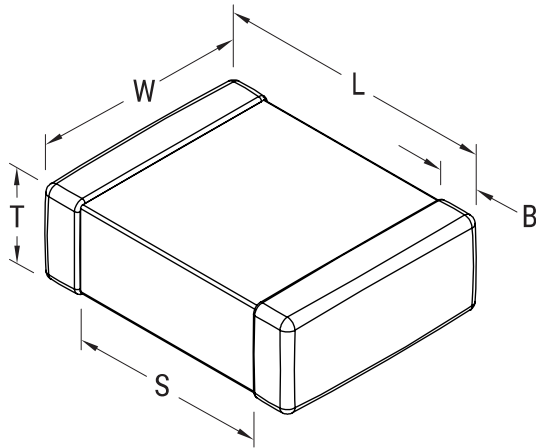


## C1206T101J5GACTU

Aliases (C1206T101J5GAC7800)

SMD COTS COG, Ceramic, 100 pF, 5%, 50 VDC, COG, SMD, MLCC, COTS, Ultra-Stable, Low Loss, Class I, 1206, 1.5 mm



Click [here](#) for the 3D model.

### General Information

|                          |                                                  |
|--------------------------|--------------------------------------------------|
| Series                   | SMD COTS COG                                     |
| Style                    | SMD Chip                                         |
| Description              | SMD, MLCC, COTS, Ultra-Stable, Low Loss, Class I |
| Features                 | Ultra-Stable, Low Loss, Class I                  |
| RoHS                     | Yes                                              |
| Termination              | Tin                                              |
| Marking                  | No                                               |
| Failure Rate             | Testing per MIL-PRF-55681 PDA 8%                 |
| AEC-Q200                 | No                                               |
| Typical Component Weight | 15 mg                                            |
| Shelf Life               | 78 Weeks                                         |
| MSL                      | 1                                                |

### Specifications

|                                                                    |                        |
|--------------------------------------------------------------------|------------------------|
| Capacitance                                                        | 100 pF                 |
| Measurement Condition                                              | 1 MHz 1.0Vrms          |
| Tolerance                                                          | 5%                     |
| Voltage DC                                                         | 50 VDC                 |
| Dielectric Withstanding Voltage                                    | 125 VDC                |
| Temperature Range                                                  | -55/+125°C             |
| Temp. Coefficient                                                  | COG                    |
| Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC) | 30 ppm/C, 1MHz 1.0Vrms |
| Dissipation Factor                                                 | 0.1% 1 MHz 1.0Vrms     |
| Aging Rate                                                         | 0% Loss/Decade Hour    |
| Insulation Resistance                                              | 100 GOhms              |

### Dimensions

|           |                  |
|-----------|------------------|
| Chip Size | 1206             |
| L         | 3.2mm +/-0.2mm   |
| W         | 1.6mm +/-0.2mm   |
| T         | 0.78mm +/-0.10mm |
| S         | 1.5mm MIN        |
| B         | 0.5mm +/-0.25mm  |

### Packaging Specifications

|                    |                          |
|--------------------|--------------------------|
| Packaging          | T&R, 180mm, Plastic Tape |
| Packaging Quantity | 4000                     |

Statements of suitability for certain applications are based on our knowledge of typical operating conditions for such applications, but are not intended to constitute - and we specifically disclaim - any warranty concerning suitability for a specific customer application or use. This Information is intended for use only by customers who have the requisite experience and capability to determine the correct products for their application. Any technical advice inferred from this Information or otherwise provided by us with reference to the use of our products is given gratis, and we assume no obligation or liability for the advice given or results obtained.