

C1210C275K3PACTU

Aliases (C1210C275K3PAC7800)

SMD Comm X5R, Ceramic, 2.7 uF, 10%, 25 VDC, X5R, SMD, MLCC, Temperature Stable, Class II, 1210, 1.5 mm



Click [here](#) for the 3D model.

General Information

Series	SMD Comm X5R
Style	SMD Chip
Description	SMD, MLCC, Temperature Stable, Class II
Features	Temperature Stable, Class II
RoHS	Yes
Termination	Tin
Marking	No
AEC-Q200	No
Typical Component Weight	65 mg
Shelf Life	78 Weeks
MSL	1

Specifications

Capacitance	2.7 uF
Measurement Condition	1 kHz 1.0Vrms
Tolerance	10%
Voltage DC	25 VDC
Dielectric Withstanding Voltage	62.5 VDC
Temperature Range	-55/+85°C
Temp. Coefficient	X5R
Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC)	15%, 1kHz 1.0Vrms
Dissipation Factor	10% 1 kHz 1.0Vrms
Aging Rate	5% Loss/Decade Hour: Referee Time is 1000 Hours
Insulation Resistance	37 MOhms

Dimensions

Chip Size	1210
L	3.2mm +/-0.2mm
W	2.5mm +/-0.2mm
T	1.25mm +/-0.15mm
S	1.5mm MIN
B	0.5mm +/-0.25mm

Packaging Specifications

Packaging	T&R, 180mm, Plastic Tape
Packaging Quantity	2500

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