

C1210J104K5RACTU

Aliases (C1210J104K5RAC7800) SMD Comm X7R FO, Ceramic, 0.1 uF, 10%, 50 VDC, X7R, SMD, MLCC, Open Mode, Temperature Stable, 1210, 1.5 mm



Click here for the 3D model.

General Information	
Series	SMD Comm X7R FO
Style	SMD Chip
Description	SMD, MLCC, Open Mode, Temperature Stable
Features	Open Mode, Temperature Stable
RoHS	Yes
Termination	Flexible Termination
Marking	No
AEC-Q200	No
Typical Component Weight	50 mg
Shelf Life	78 Weeks
MSL	1

Dimensions		Sp
Chip Size	1210	Ca
L	3.3mm +/-0.4mm	Me
W	2.6mm +/-0.3mm	Tol
т	0.95mm +/-0.20mm	Vo
S	1.5mm MIN	Die
В	0.6mm +/-0.25mm	Ter
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Packaging Specifications

Packaging	T&R, 180mm, Plastic Tape
Packaging Quantity	4000

Specifications	
Capacitance	0.1 uF
Measurement Condition	1 kHz 1.0Vrms
Tolerance	10%
Voltage DC	50 VDC
Dielectric Withstanding Voltage	125 VDC
Temperature Range	-55/+125°C
Temp. Coefficient	X7R
Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC)	15%, 1kHz 1.0Vrms
Dissipation Factor	2.5%1kHz1.0Vrms
Aging Rate	3% Loss/Decade Hour: Referee Time is 1000 Hours
Insulation Resistance	10 GOhms

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