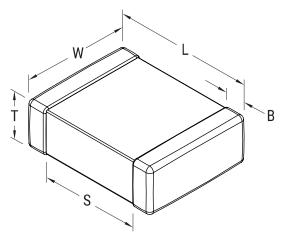


C1210J104K5RALTU

Aliases (C1210J104K5RAL7800) SMD Comm X7R FO, Ceramic, 0.1 uF, 10%, 50 VDC, X7R, SMD, MLCC, Open Mode, Temperature Stable, 1210, 1.5 mm



Click here for the 3D model.

| General Information | |
|--------------------------|---|
| Series | SMD Comm X7R FO |
| Style | SMD Chip |
| Description | SMD, MLCC, Open Mode, Temperature Stable |
| Features | Open Mode, Temperature Stable |
| RoHS | No |
| Prop 65 | WARNING: Cancer and reproductive harm - https://www.p65warnings.ca.gov / |
| SCIP Number | 935062e8-aaf1-4654-aced-354 52904a0dc |
| Termination | Flexible Termination With Lead (SnPb) |
| Marking | No |
| AEC-Q200 | No |
| Typical Component Weight | 50 mg |
| Shelf Life | 78 Weeks |
| MSL | 1 |

| Dimensions | |
|------------|------------------|
| Chip Size | 1210 |
| L | 3.3mm +/-0.4mm |
| W | 2.6mm +/-0.3mm |
| т | 0.95mm +/-0.20mm |
| S | 1.5mm MIN |
| В | 0.6mm +/-0.25mm |

| Packaging Specifications | |
|--------------------------|--------------------------|
| Packaging | T&R, 180mm, Plastic Tape |
| Packaging Quantity | 4000 |

| Specifications | |
|--|--|
| Capacitance | 0.1 uF |
| Measurement Condition | 1 kHz 1.0Vrms |
| Tolerance | 10% |
| Voltage DC | 50 VDC |
| Dielectric Withstanding Voltage | 125 VDC |
| Temperature Range | -55/+125°C |
| Temp. Coefficient | X7R |
| Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC) | 15%, 1kHz 1.0Vrms |
| Dissipation Factor | 2.5%1kHz1.0Vrms |
| Aging Rate | 3% Loss/Decade Hour: Referee Time is 1000 Hours |
| Insulation Resistance | 10 GOhms |

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