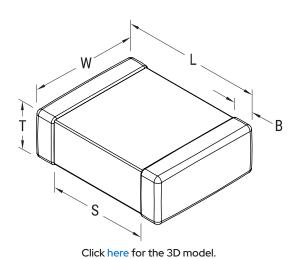


C1812C394K3JACTU

Specifications

Insulation Resistance

Aliases (C1812C394K3JAC7800) SMD Comm U2J, Ceramic, 0.39 uF, 10%, 25 VDC, U2J, SMD, MLCC, Ultra-Stable, Low Loss, Class I, 1812, 2.3 mm



General Information	
Series	SMD Comm U2J
Style	SMD Chip
Description	SMD, MLCC, Ultra-Stable, Low Loss, Class I
Features	Ultra-Stable, Low Loss, Class I
RoHS	Yes
Termination	Tin
Marking	No
AEC-Q200	No
Typical Component Weight	67 mg
Shelf Life	78 Weeks
MSL	1

Dimensions	
Chip Size	1812
L	4.5mm +/-0.3mm
W	3.2mm +/-0.3mm
Т	1.4mm +/-0.15mm
S	2.3mm MIN
В	0.6mm +/-0.35mm

	Capacitance	0.39 uF
	Measurement Condition	1 kHz 1.0Vrms
	Tolerance	10%
	Voltage DC	25 VDC
	Dielectric Withstanding Voltage	62.5 VDC
	Temperature Range	-55/+125°C
	Temp. Coefficient	U2J
	Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC)	-750+/-120 ppm/C, 1kHz 1.0Vrms
	Dissipation Factor	0.1% 1 kHz 1.0Vrms
	Aging Rate	0.1% Loss/Decade Hour: Referee

Time is 1000 Hours 2.5641 GOhms

Packaging Specifications	
Packaging	T&R, 180mm, Plastic Tape
Packaging Quantity	1000

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