



Click [here](#) for the 3D model.

General Information

Series	KONNEKT Auto X7R
Style	KONNEKT
Description	SMD, MLCC, KONNEKT, Ultra-Stable, Class II
Features	High Density Packaging
RoHS	Yes
Termination	Tin
Qualifications	AEC-Q200
AEC-Q200	Yes
Typical Component Weight	350 mg
Chip Size	1812-2
Shelf Life	78 Weeks
MSL	1

Specifications

Capacitance	9,400 pF
Measurement Condition	120 Hz 0.5Vrms
Tolerance	10%
Voltage DC	2500 VDC
Dielectric Withstanding Voltage	3,000 VDC
Temperature Range	-55/+125°C
Temp. Coefficient	X7R
Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC)	15%, 1kHz 1.0Vrms
Dissipation Factor	2.5% 1 kHz 1.0Vrms
Aging Rate	3% Loss/Decade Hour
Insulation Resistance	100 GOhms

Dimensions

L	4.5mm +/-0.3mm
W	3.2mm +/-0.3mm
T	5.1mm +/-0.4mm
B	0.6mm +/-0.35mm

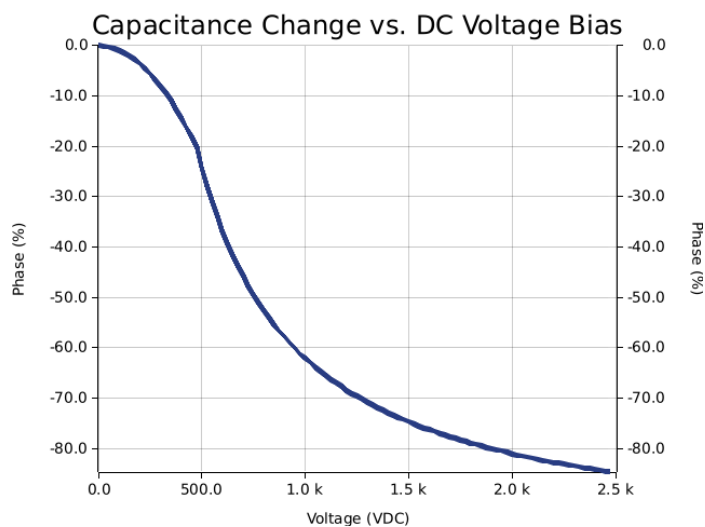
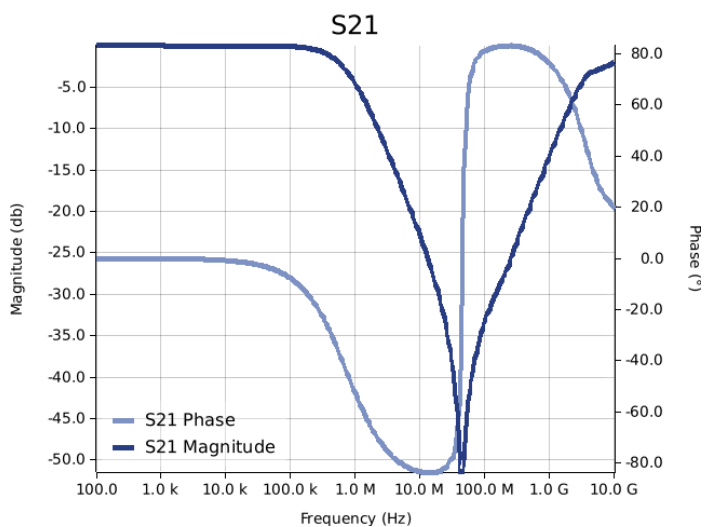
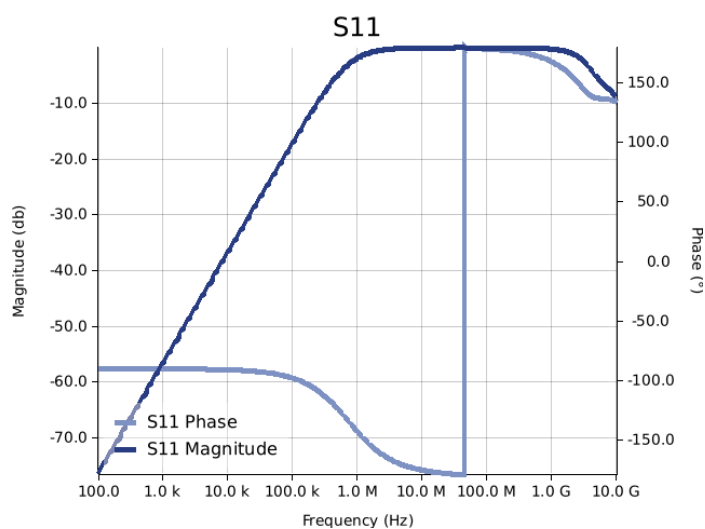
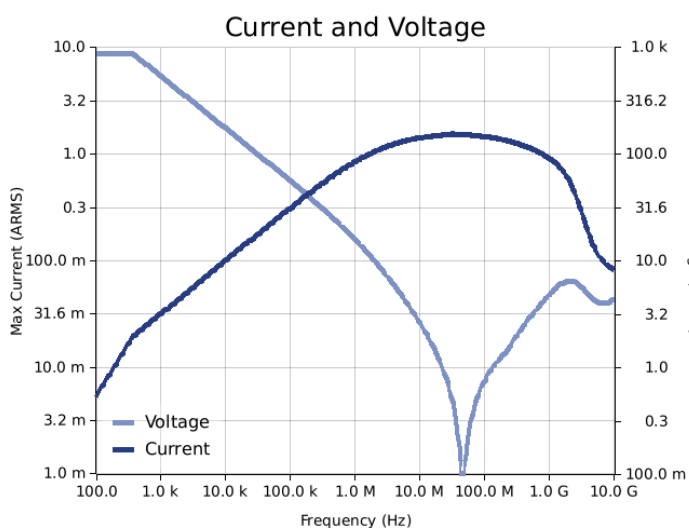
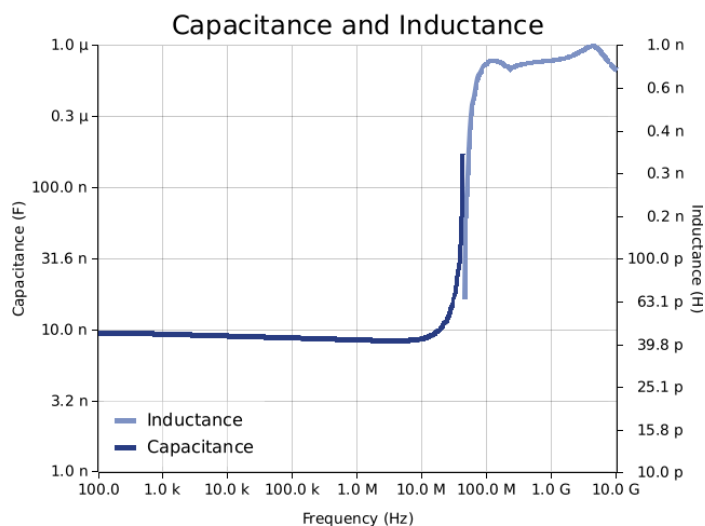
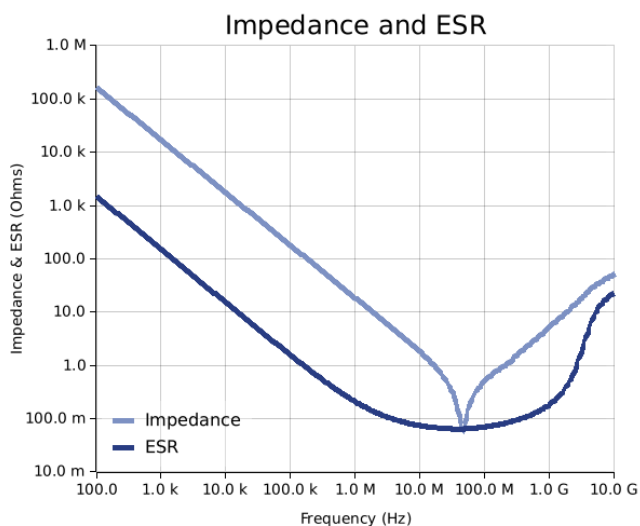
Packaging Specifications

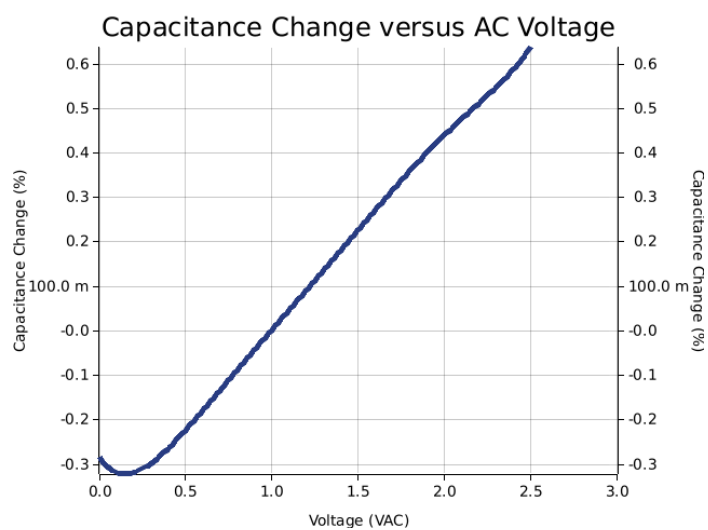
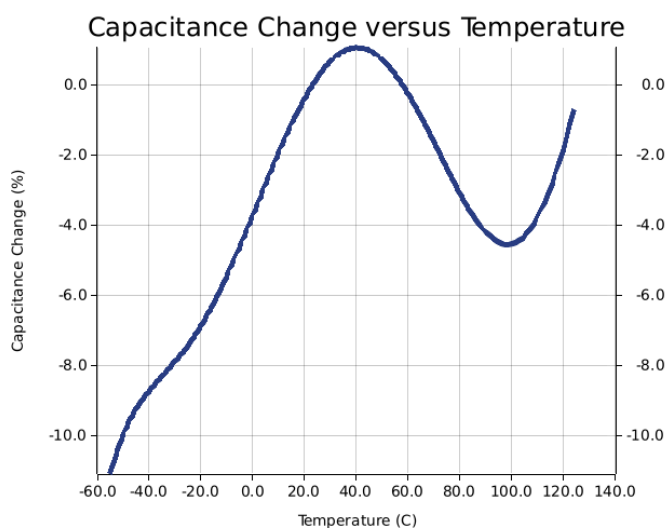
Packaging	T&R, 180mm, Plastic Tape
Packaging Quantity	200

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Simulations

For the complete simulation environment please visit [K-SIM](#).





These are simulations.
This is not a specification!

The responses shown represent the typical response for each part type. Specific responses may vary, depending on manufacturing variation effects of all parameters involved, including the specified tolerances applied to capacitance and unspecified variations of ESR, ESL, and leakage resistance.

The responses shown do not represent a specified or implied maximum capability of the device for all applications.

- The ESR used for ripple "Ripple Current/Voltage vs. Frequency" plots is the ESR at ambient temperature.
- The ESR in the "Temperature Rise vs. Ripple Current" plots is adjusted to each incremental temperature rise before the power and ripple current is calculated.
- The effects shown herein are based on measured data from a multiple part sample of the parts in question.
- Ripple capability of this device will be factored by thermal resistance (R_{th}) created by circuit traces (addi affects of all parameters involved, including the specified tolerances applied to capacitance and unspecified variations of ESR, ESL, and leakage resistance).
- The peak voltages generated in the "Temperature Rise vs. Combined Ripple Currents" plot are calculated for each frequency and are not combined with voltages generated at any other harmonics.
- Please consult with the catalog or field applications engineer for maximum capability of the device in specific applications.

All product information and data (collectively, the "Information") are subject to change without notice.

KEMET K-SIM is designed to simulate behavior of components with respect to frequency, ambient temperature, and DC bias levels. The responses shown represent the typical response for each part type. Specific responses may vary, depending on manufacturing variation effects of all parameters involved, including the specified tolerances applied to capacitance and unspecified variations of ESR, ESL, and leakage resistance.

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If you have any questions please contact K-SIM.