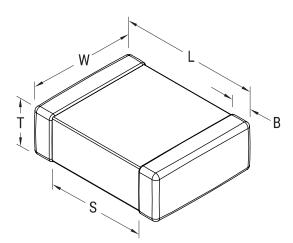


C1812H104J1GACTU

Aliases (C1812H104J1GAC7800) SMD Indust COG HT200C, Ceramic, 0.1 uF, 5%, 100 VDC, COG, SMD, MLCC, High Temperature, Ultra-Stable, Low Loss, 1812, 2.3 mm



Click here for the 3D model.

Dimensions	
Chip Size	1812
L	4.5mm +/-0.3mm
W	3.2mm +/-0.3mm
т	1.25mm +/-0.15mm
S	2.3mm MIN
В	0.6mm +/-0.35mm

Packaging Specifications

Packaging	T&R, 180mm, Plastic Tape
Packaging Quantity	1000

General Information	
Series	SMD Indust COG HT200C
Style	SMD Chip
Description	SMD, MLCC, High Temperature, Ultra-Stable, Low Loss
Features	High Temp, Ultra-Stable, Low Loss
RoHS	Yes
Termination	Tin
Marking	No
AEC-Q200	No
Typical Component Weight	67 mg
Shelf Life	78 Weeks
MSL	1

Specifications	
Capacitance	0.1 uF
Measurement Condition	1 kHz 1.0Vrms
Tolerance	5%
Voltage DC	100 VDC
Dielectric Withstanding Voltage	250 VDC
Temperature Range	-55/+200°C
Temp. Coefficient	COG
Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC)	30 ppm/C, 1kHz 1.0Vrms
Dissipation Factor	0.1% 1 kHz 1.0Vrms
Aging Rate	0% Loss/Decade Hour
Insulation Resistance	10 GOhms

Statements of suitability for certain applications are based on our knowledge of typical operating conditions for such applications, but are not intended to constitute - and we specifically disclaim - any warranty concerning suitability for a specific customer application or use. This Information is intended for use only by customers who have the requisite experience and capability to determine the correct products for their application. Any technical advice inferred from this Information or otherwise provided by us with reference to the use of our products is given gratis, and we assume no obligation or liability for the advice given or results obtained.