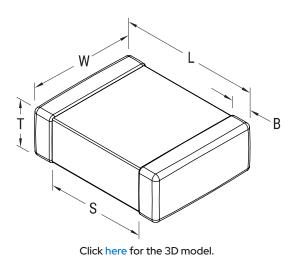


## C1812T224J5GACTU

Aliases (C1812T224J5GAC7800) SMD COTS COG, Ceramic, 0.22 uF, 5%, 50 VDC, COG, SMD, MLCC, COTS, Ultra-Stable, Low Loss, Class I, 1812, 2.3 mm



General Information	
Series	SMD COTS COG
Style	SMD Chip
Description	SMD, MLCC, COTS, Ultra-Stable, Low Loss, Class I
Features	Ultra-Stable, Low Loss, Class I
RoHS	Yes
Termination	Tin
Marking	No
Failure Rate	Testing per MIL-PRF-55681 PDA 8%
AEC-Q200	No
Typical Component Weight	87 mg
Shelf Life	78 Weeks
MSL	1

Dimensions	
Chip Size	1812
L	4.5mm +/-0.3mm
W	3.2mm +/-0.3mm
Т	1.6mm +/-0.20mm
S	2.3mm MIN
В	0.6mm +/-0.35mm

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Т	1.6mm +/-0.20mm
S	2.3mm MIN
В	0.6mm +/-0.35mm
Packaging Specifications	

1000

T&R, 180mm, Plastic Tape

Packaging

Packaging Quantity

Specifications	
Capacitance	0.22 uF
Measurement Condition	1 kHz 1.0Vrms
Tolerance	5%
Voltage DC	50 VDC
Dielectric Withstanding Voltage	125 VDC
Temperature Range	-55/+125°C
Temp. Coefficient	COG
Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC)	30 ppm/C, 1kHz 1.0Vrms
Dissipation Factor	0.1% 1 kHz 1.0Vrms
Aging Rate	0% Loss/Decade Hour
Insulation Resistance	4.5455 GOhms

Statements of suitability for certain applications are based on our knowledge of typical operating conditions for such applications, but are not intended to constitute - ar	nd
we specifically disclaim - any warranty concerning suitability for a specific customer application or use. This Information is intended for use only by customers who have t	the
requisite experience and capability to determine the correct products for their application. Any technical advice inferred from this Information or otherwise provided by	us
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