

C1812T475K5RCLTU

Aliases (C1812T475K5RCL7800)

SMD COTS X7R, Ceramic, 4.7 uF, 10%, 50 VDC, X7R, SMD, MLCC, COTS, Temperature Stable, Class II, 1812, 2.3 mm



Click [here](#) for the 3D model.

General Information

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|--------------------------|--|
| Series | SMD COTS X7R |
| Style | SMD Chip |
| Description | SMD, MLCC, COTS, Temperature Stable, Class II |
| Features | Temperature Stable, Class II |
| RoHS | No |
| Prop 65 | WARNING: Cancer and reproductive harm - https://www.p65warnings.ca.gov/ |
| SCIP Number | 2d771165-5336-48a3-96fa-3663929fd828 |
| Termination | Lead (SnPb) |
| Marking | No |
| Failure Rate | Testing per MIL-PRF-55681 PDA 8%, DPA per EIA-469, Humidity per MIL-STD-202, Method 103, Condition A |
| AEC-Q200 | No |
| Typical Component Weight | 185 mg |
| Shelf Life | 78 Weeks |
| MSL | 1 |

Dimensions

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|-----------|-----------------|
| Chip Size | 1812 |
| L | 4.5mm +/-0.3mm |
| W | 3.2mm +/-0.3mm |
| T | 1.6mm +/-0.20mm |
| S | 2.3mm MIN |
| B | 0.6mm +/-0.35mm |

Packaging Specifications

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|--------------------|--------------------------|
| Packaging | T&R, 180mm, Plastic Tape |
| Packaging Quantity | 1000 |

Specifications

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|--|---|
| Capacitance | 4.7 uF |
| Measurement Condition | 1 kHz 1.0Vrms |
| Tolerance | 10% |
| Voltage DC | 50 VDC |
| Dielectric Withstanding Voltage | 125 VDC |
| Temperature Range | -55/+125°C |
| Temp. Coefficient | X7R |
| Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC) | 15%, 1kHz 1.0Vrms |
| Dissipation Factor | 2.5% 1 kHz 1.0Vrms |
| Aging Rate | 3% Loss/Decade Hour: Referee Time is 1000 Hours |
| Insulation Resistance | 106.4 MOhms |

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