



Click [here](#) for the 3D model.

### Dimensions

L	31.5mm +0.5/-0.7mm
H	20mm +0.2/-0.7mm
T	11mm +0.3/-0.7mm
S	27.5mm +/-0.4mm
LL	6mm +0/-2mm
F	0.8mm +/-0.05mm

### Packaging Specifications

Packaging	Bulk, Box
Packaging Quantity	256

### General Information

Series	C4AQ-M
Dielectric	Metallized Polypropylene
Style	Radial
RoHS	Yes
Lead	Wire Leads
Qualifications	AEC-Q200, IEC61071, EN61071, VDE0560
AEC-Q200	Yes
Typical Component Weight	11g
Miscellaneous	Rth = 44 C/W.

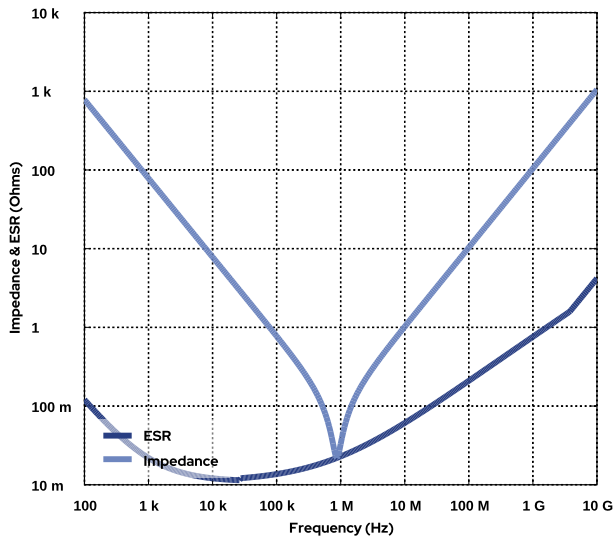
### Specifications

Capacitance	2 uF
Capacitance Tolerance	5%
Voltage DC	900 VDC
Temperature Range	-55/+105°C
Rated Temperature	85°C
Insulation Resistance	15 GOhms
Max dV/dt	70 V/us
ESR	21.9 mOhms (10kHz 70C)
Ripple Current	5.2 Amps Irms (10kHz 70C), 140 Amps Ipk
Inductance	17 nH

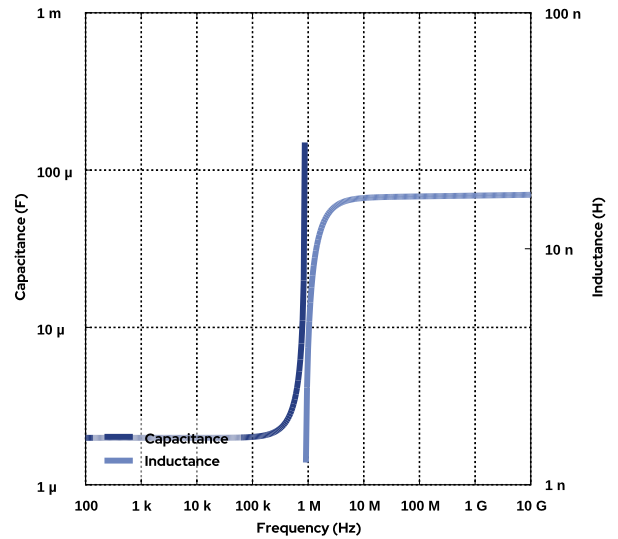
## Simulations

For the complete simulation environment please visit [K-SIM](#).

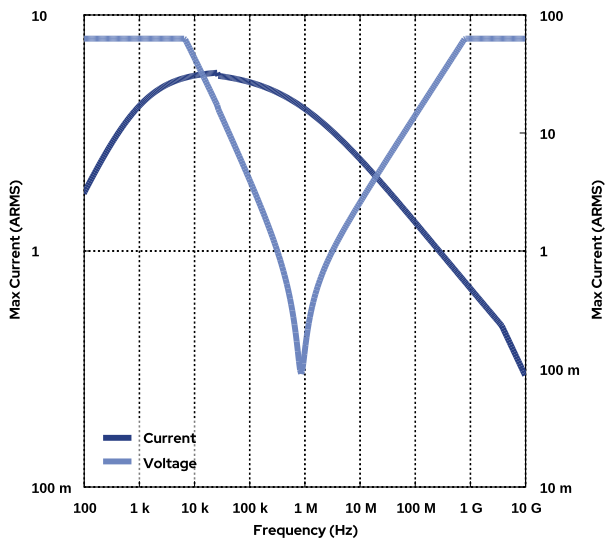
**Impedance and ESR**



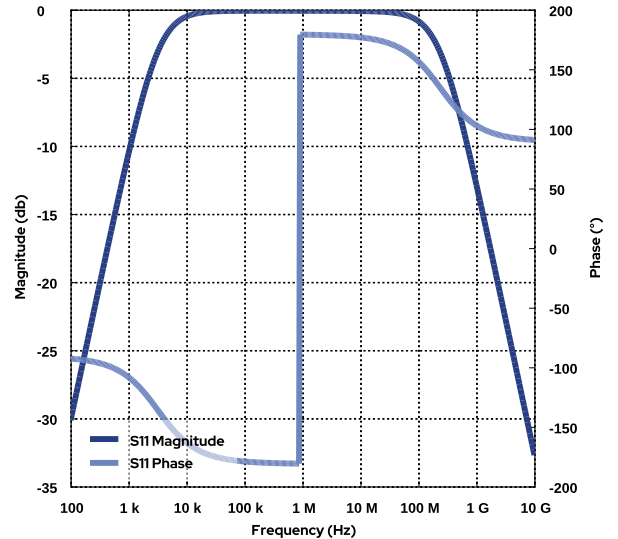
**Capacitance and Inductance**

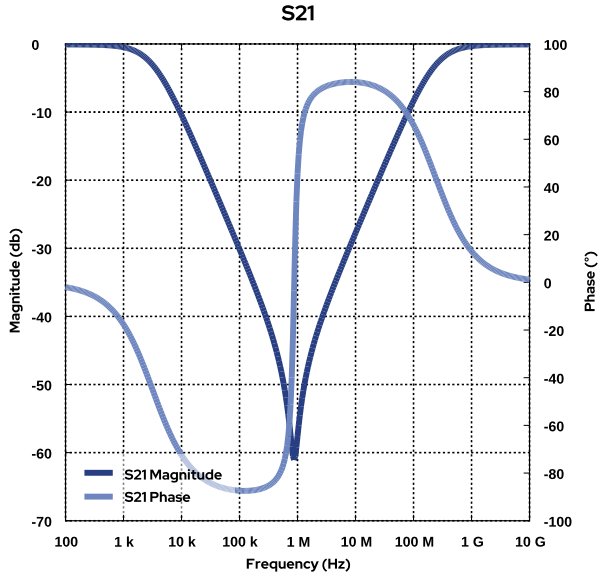


**Current and Voltage**



**S11**





**These are simulations.**

This is not a specification!

The responses shown represent the typical response for each part type. Specific responses may vary, depending on manufacturing variation affects of all parameters involved, including the specified tolerances applied to capacitance and unspecified variations of ESR, ESL, and leakage resistance.

The responses shown do not represent a specified or implied maximum capability of the device for all applications.

- The ESR used for ripple "Ripple Current/Voltage vs. Frequency" plots is the ESR at ambient temperature.
- The ESR in the "Temperature Rise vs. Ripple Current" plots is adjusted to each incremental temperature rise before the power and ripple current is calculated.
- The effects shown herein are based on measured data from a multiple part sample of the parts in question.
- Ripple capability of this device will be factored by thermal resistance (Rth) created by circuit traces (addi affects of all parameters involved, including the specified tolerances applied to capacitance and unspecified variations of ESR, ESL, and leakage resistance.
- The peak voltages generated in the "Temperature Rise vs. Combined Ripple Currents" plot are calculated for each frequency and are not combined with voltages generated at any other harmonics.
- Please consult with the catalog or field applications engineer for maximum capability of the device in specific applications.

All product information and data (collectively, the "Information") are subject to change without notice.

KEMET K-SIM is designed to simulate behavior of components with respect to frequency, ambient temperature, and DC bias levels. The responses shown represent the typical response for each part type. Specific responses may vary, depending on manufacturing variation effects of all parameters involved, including the specified tolerances applied to capacitance and unspecified variations of ESR, ESL, and leakage resistance.

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If you have any questions please contact K-SIM.