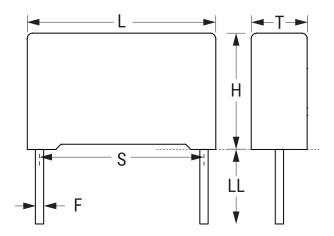


C4AUIBU4240M1BJ

C4AU, Film, Metallized Polypropylene, Automotive DC Link, 2.4 uF, 5%, 800 VDC, 27.5 mm



Click here for the 3D model.

Dimensions	
L	32mm +/-0.7mm
н	22mm +/-0.7mm
т	13mm +/-0.7mm
S	27.5mm +/-0.4mm
LL	6mm -2mm
F	0.8mm +/-0.05mm

Packaging SpecificationsPackagingBulk, BoxPackaging Quantity208

General Information	
Series	C4AU
Dielectric	Metallized Polypropylene
Style	Radial
Features	DC Filtering
RoHS	Yes
Lead	Wire Leads
Qualifications	AEC-Q200, IEC61071, EN61071, VDE0560
AEC-Q200	Yes
Typical Component Weight	14.9 g
Miscellaneous	Rth = 39 C/W.

Specifications	
Capacitance	2.4 uF
Tolerance	5%
Voltage DC	800 VDC
Temperature Range	-55/+105°C
Insulation Resistance	12.5 GOhms
Max dV/dt	65 V/us
ESR	18.1 mOhms (10kHz 70C)
Ripple Current	6.1 Amps Irms (10kHz 70C), 156 Amps Ipkr
Inductance	19 nH

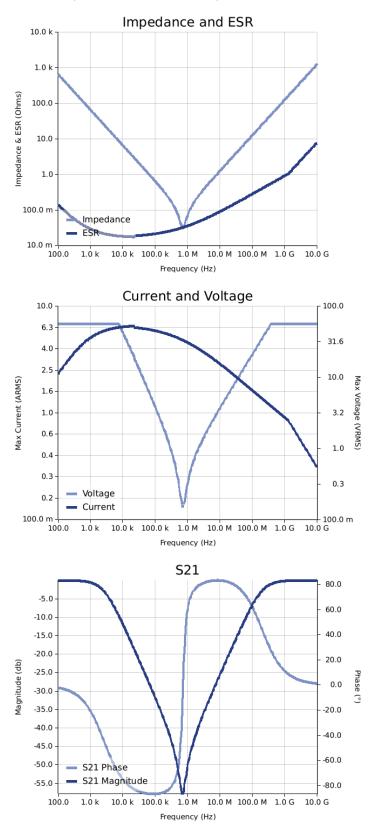
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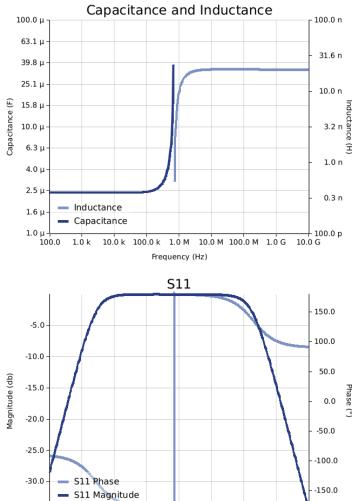


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Simulations

For the complete simulation environment please visit K-SIM.





1.0 k 10.0 k 100.0 k 1.0 M 10.0 M 100.0 M 1.0 G

Frequency (Hz)

100.0

10.0 G



These are simulations.

This is not a specification!

The responses shown represent the typical response for each part type. Specific responses may vary, depending on manufacturing variation affects of all parameters involved, including the specified tolerances applied to capacitance and unspecified variations of ESR, ESL, and leakage resistance.

The responses shown do not represent a specified or implied maximum capability of the device for all applications.

- The ESR used for ripple "Ripple Current/Voltage vs. Frequency" plots is the ESR at ambient temperature.

- The ESR used for hipple klipple current younge vs. requericy plots is the ESR at an bient temperature.
 The ESR in the "Temperature Rise vs. Ripple Current" plots is adjusted to each incremental temperature rise before the power and ripple current is calculated.
 The effects shown herein are based on measured data from a multiple part sample of the parts in question.
 Ripple capability of this device will be factored by thermal resistance (Rth) created by circuit traces (addi affects of all parameters involved, including the specified tolerances applied to capacitance and unspecified variations of ESR, ESL, and leakage resistance.
 The peak voltages generated in the "Temperature Rise vs. Combined Ripple Currents" plot are calculated for each frequency and are not combined with voltages and the part of the parts of the part of the
- generated at any other harmonics.
 Please consult with the catalog or field applications engineer for maximum capability of the device in specific applications.

All product information and data (collectively, the "Information") are subject to change without notice.

KEMET K-SIM is designed to simulate behavior of components with respect to frequency, ambient temperature, and DC bias levels. The responses shown represent the typical response for each part type. Specific responses may vary, depending on manufacturing variation effects of all parameters involved, including the specified tolerances applied to capacitance and unspecified variations of ESR, ESL, and leakage resistance.

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If you have any questions please contact K-SIM.