



Click [here](#) for the 3D model.

### Dimensions

|     |                 |
|-----|-----------------|
| D   | 10.5mm +/-0.5mm |
| L   | 8.5mm MAX       |
| W   | 10.8mm +/-0.2mm |
| S   | 5mm NOM         |
| G   | 0mm +0.3/-0.1mm |
| LT1 | 1.2mm +/-0.1mm  |
| LT2 | 3.6mm +0.5mm    |

### Packaging Specifications

|           |            |
|-----------|------------|
| Lead      | V-Chip     |
| Packaging | T&R, 380mm |

### General Information

|                          |                                      |
|--------------------------|--------------------------------------|
| Series                   | FC                                   |
| Description              | Surface Mount Double Layer Capacitor |
| RoHS                     | Yes                                  |
| AEC-Q200                 | No                                   |
| Typical Component Weight | 1.4 g                                |

### Specifications

|                       |  |
|-----------------------|--|
| Capacitance           | 0.22 F                                 |
| Capacitance Tolerance | -20/+80%                               |
| Voltage DC            | 5.5 VDC, 4.2 VDC (Holding Voltage)     |
| Temperature Range     | -25/+70°C                              |
| Rated Temperature     | 70°C                                   |
| ESR                   | 25000 mOhms (1kHz)                     |
| Discharge Current     | 0.33 mAmps (30 Minutes After Charging) |

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