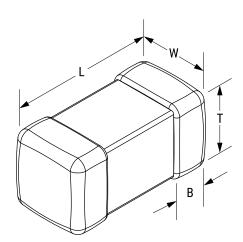


L0402C56NJRMST

Aliases (LO402C56NJRMST)

Obsolete

KEMET, L-RMS, Ceramic, 5%, 56 nH



| General Information | |
|---------------------|------------------------|
| Series | L-RMS |
| Style | SMD Multi-Layer |
| Core | Ceramic |
| Description | Surface Mount Inductor |
| RoHS | Yes |

Click here for the 3D model.

| Dimensions | |
|--------------------------|-----------------|
| L | 1mm +/-0.05mm |
| W | 0.5mm +/-0.05mm |
| Т | 0.5mm +/-0.05mm |
| | |
| Packaging Specifications | |
| Packaging | T&R |
| Packaging Quantity | 10000 |
| | |

| Specifications | |
|-------------------------|--|
| Inductance | 0.056 uH (100 MHz) |
| Inductance Tolerance | 5% |
| Inductance | 56 nH |
| Rated Current | 200 mAmps (MAX) |
| Temperature Range | -55/+125°C |
| DC Resistance | 1 Ohms |
| DC Resistance | 1 Ohms |
| Quality Factor | 11 at 100 MHz, 18 at 500 MHz, 18 at 800 MHz, 16 at 1000 MHz |
| Self-Resonant Frequency | 750 MHz |

Statements of suitability for certain applications are based on our knowledge of typical operating conditions for such applications, but are not intended to constitute – and we specifically disclaim – any warranty concerning suitability for a specific customer application or use. This Information is intended for use only by customers who have the requisite experience and capability to determine the correct products for their application. Any technical advice inferred from this Information or otherwise provided by us with reference to the use of our products is given gratis, and we assume no obligation or liability for the advice given or results obtained.

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