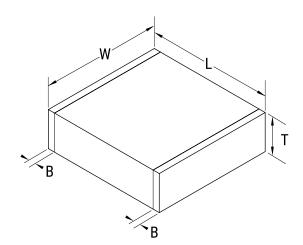


LDEID2180KA5N00

Aliases (DEID2180KA5N00) LDE, Film, Metallized PEN Stacked, Automotive Grade, 0.018 uF, 10%, 250 VDC, 120 VAC, 125°C, 2220, 2.2 mm



General Information	
Series	LDE
Dielectric	Metallized PEN Stacked
Style	SMD Chip
Features	Automotive Grade
RoHS	Yes
Qualifications	AEC-Q200
AEC-Q200	Yes
Shelf Life	104 Weeks
MSL	2a

Click here for the 3D model.

Dimensions	
Chip Size	2220
L	6mm +/-0.3mm
W	5mm +/-0.4mm
т	2.2mm MAX
В	0.4mm +0.5/-0.3mm

Packaging Specifications	
Packaging	T&R
Packaging Quantity	3000

Specifications	
Capacitance	0.018 uF
Tolerance	10%
Voltage DC	250 VDC
Voltage AC	120 VAC
Temperature Range	-55/+125°C
Rated Temperature	125°C
Dissipation Factor	0.8% 1kHz
Insulation Resistance	1GOhms
Max dV/dt	100 V/us

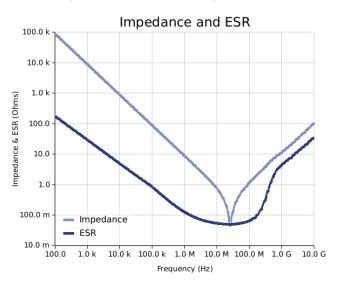
Statements of suitability for certain applications are based on our knowledge of typical operating conditions for such applications, but are not intended to constitute - and we specifically disclaim - any warranty concerning suitability for a specific customer application or use. This Information is intended for use only by customers who have the requisite experience and capability to determine the correct products for their application. Any technical advice inferred from this Information or otherwise provided by us with reference to the use of our products is given gratis, and we assume no obligation or liability for the advice given or results obtained.

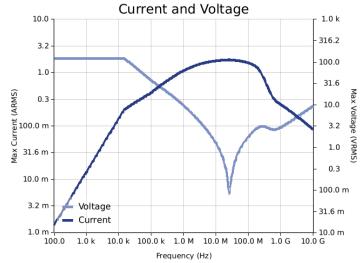


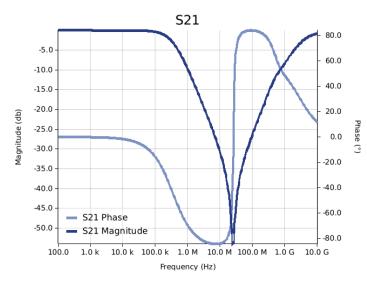
LDEID2180KA5N00 Aliases (DEID2180KA5N00) LDE, Film, Metallized PEN Stacked, Automotive Grade, 0.018 uF, 10%, 250 VDC, 120 VAC, 125°C, 2220, 2.2 mm

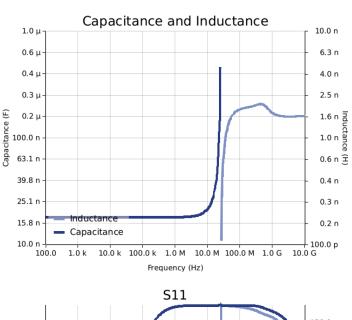
Simulations

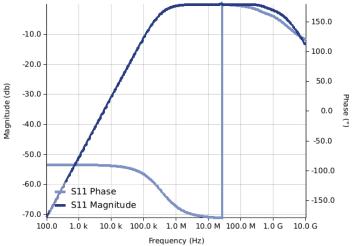
For the complete simulation environment please visit K-SIM.













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LDE, Film, Metallized PEN Stacked, Automotive Grade, 0.018 uF, 10%, 250 VDC, 120 VAC, 125°C, 2220, 2.2 mm

These are simulations.

This is not a specification!

The responses shown represent the typical response for each part type. Specific responses may vary, depending on manufacturing variation affects of all parameters involved, including the specified tolerances applied to capacitance and unspecified variations of ESR, ESL, and leakage resistance.

The responses shown do not represent a specified or implied maximum capability of the device for all applications.

- The ESR used for ripple "Ripple Current/Voltage vs. Frequency" plots is the ESR at ambient temperature.

- The ESR used for hipple klipple current younge vs. requericy plots is the ESR at an bient temperature.
 The ESR in the "Temperature Rise vs. Ripple Current" plots is adjusted to each incremental temperature rise before the power and ripple current is calculated.
 The effects shown herein are based on measured data from a multiple part sample of the parts in question.
 Ripple capability of this device will be factored by thermal resistance (Rth) created by circuit traces (addi affects of all parameters involved, including the specified tolerances applied to capacitance and unspecified variations of ESR, ESL, and leakage resistance.
 The peak voltages generated in the "Temperature Rise vs. Combined Ripple Currents" plot are calculated for each frequency and are not combined with voltages and the part of the parts of the part of the
- generated at any other harmonics.
 Please consult with the catalog or field applications engineer for maximum capability of the device in specific applications.

All product information and data (collectively, the "Information") are subject to change without notice.

KEMET K-SIM is designed to simulate behavior of components with respect to frequency, ambient temperature, and DC bias levels. The responses shown represent the typical response for each part type. Specific responses may vary, depending on manufacturing variation effects of all parameters involved, including the specified tolerances applied to capacitance and unspecified variations of ESR, ESL, and leakage resistance.

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If you have any questions please contact K-SIM.