

R76TW3220JH30K

Aliases (76TW3220JH30K) R76, Film, Double Metallized Polypropylene, General Purpose, 0.22 uF, 10%, 1,600 VDC, 85°C, 37.5 mm



General Information	
Series	R76
Dielectric	Double Metallized Polypropylene
Style	Radial
Features	Pulse
RoHS	Yes
Termination	Cut (Tinned Wire)
Lead	Cut
AEC-Q200	No
Typical Component Weight	15.54 g

Specifications 0.22 uF Capacitance 10% Tolerance Voltage DC 1600 VDC 650 VAC Voltage AC **Temperature Range** -55/+110°C **Rated Temperature** 85°C **Dissipation Factor** 0.03% lkHz, 0.06% l0kHz Insulation Resistance 100 GOhms Max dV/dt 1,200 V/us ESR 10.85 mOhms (100kHz) 8.5 Amps (100kHz 85C), 264 Amps (Peak) **Ripple Current** Inductance 20 nH

 Dimensions

 L
 41.5mm +0.3/-0.7mm

 H
 24mm +0.1/-0.7mm

 T
 13mm +0.3/-0.7mm

 S
 37.5mm +/-0.4mm

 LL
 3.2mm +0.3/-0.2mm

 F
 1mm +/-0.05mm

Click here for the 3D model.

Packaging Specifications		
Packaging	Tray	
Packaging Quantity	360	

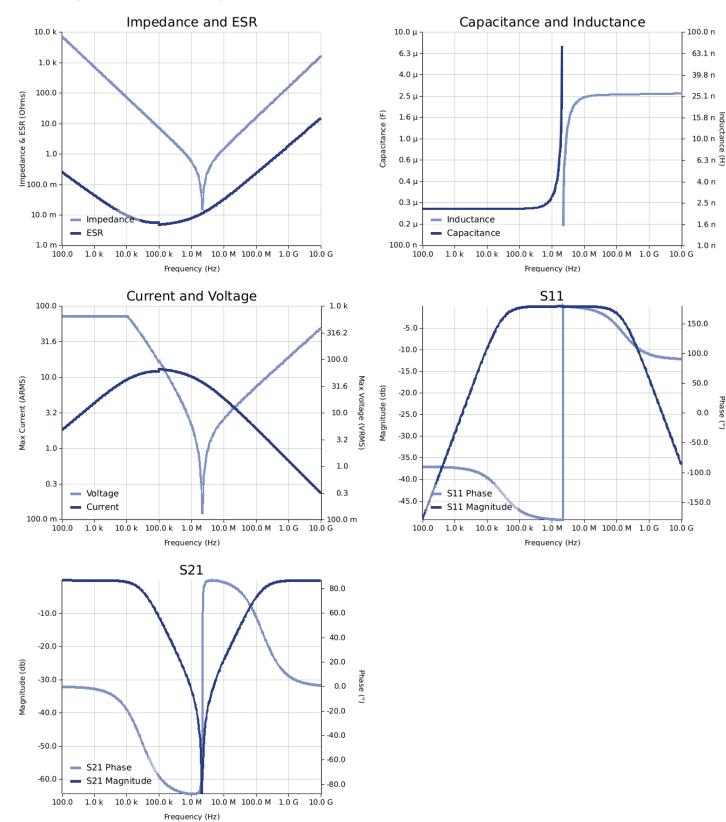
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Simulations

For the complete simulation environment please visit K-SIM.



tan

Phase (°)



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These are simulations.

This is not a specification!

The responses shown represent the typical response for each part type. Specific responses may vary, depending on manufacturing variation affects of all parameters involved, including the specified tolerances applied to capacitance and unspecified variations of ESR, ESL, and leakage resistance.

The responses shown do not represent a specified or implied maximum capability of the device for all applications.

- The ESR used for ripple "Ripple Current/Voltage vs. Frequency" plots is the ESR at ambient temperature.

- The ESR in the "Temperature Rise vs. Ripple Current" plots is adjusted to each incremental temperature rise before the power and ripple current is calculated. The effects shown herein are based on measured data from a multiple part sample of the parts in question. Ripple capability of this device will be factored by thermal resistance (Rth) created by circuit traces (addi affects of all parameters involved, including the specified tolerances applied to capacitance and unspecified variations of ESR, ESL, and leakage resistance. The peak voltages generated in the "Temperature Rise vs. Combined Ripple Currents" plot are calculated for each frequency and are not combined with voltages
- generated at any other harmonics. Please consult with the catalog or field applications engineer for maximum capability of the device in specific applications.

All product information and data (collectively, the "Information") are subject to change without notice.

KEMET K-SIM is designed to simulate behavior of components with respect to frequency, ambient temperature, and DC bias levels. The responses shown represent the typical response for each part type. Specific responses may vary, depending on manufacturing variation effects of all parameters involved, including the specified tolerances applied to capacitance and unspecified variations of ESR, ESL, and leakage resistance.

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If you have any questions please contact K-SIM.