

T495X477K006ATE060

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T495, Tantalum, MnO2 Tantalum, Commercial Grade, 470 uF, 10%, 6.3 VDC, SMD, MnO2, Molded, Low ESR, 60 mOhms, 7343, 4.3 mm, 1.3 mm

CATHODE (-) END VIEW

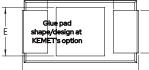


ANODE (+) END VIEW

Dime

L W T S F A B C G R X

SIDE VIEW



Click here for the 3D model.

General Information	
Series	T495
Dielectric	MnO2 Tantalum
Style	SMD Chip
Description	SMD, MnO2, Molded, Low ESR
Features	Low ESR
RoHS	Yes
Termination	Tin
AEC-Q200	No
Typical Component Weight	652.04 mg
Shelf Life	156 Weeks
MSL	1

ensions		Specif
	7.3mm +/-0.3mm	Capaci
	4.3mm +/-0.3mm	Tolerar
	4mm +/-0.3mm	Voltage
	0.13mm REF	Tempe
	1.3mm +/-0.3mm	Rated
	2.4mm +/-0.1mm	Dissipa
	3.6mm MIN	Failure
	0.5mm +/-0.15mm	ESR
	3.5mm REF	Ripple
	3.5mm REF	
	1.7mm REF	Leakag
	1mm REF	
	0.1mm +/-0.1mm REF	

Specifications	
Capacitance	470 uF
Tolerance	10%
Voltage DC	6.3 VDC (85C), 4.22 VDC (125C)
Temperature Range	-55/+125°C
Rated Temperature	85°C
Dissipation Factor	10% 120Hz 25C
Failure Rate	N/A
ESR	60 mOhms (100kHz 25C)
Ripple Current	1658 mA (rms, 100kHz 25C), 1492.2 mA (rms, 85C), 663.2 mA (rms, 125C)
Leakage Current	29.6 uA (5min 25°C)

Packaging Specifications	
Packaging	T&R, 178mm
Packaging Quantity	500

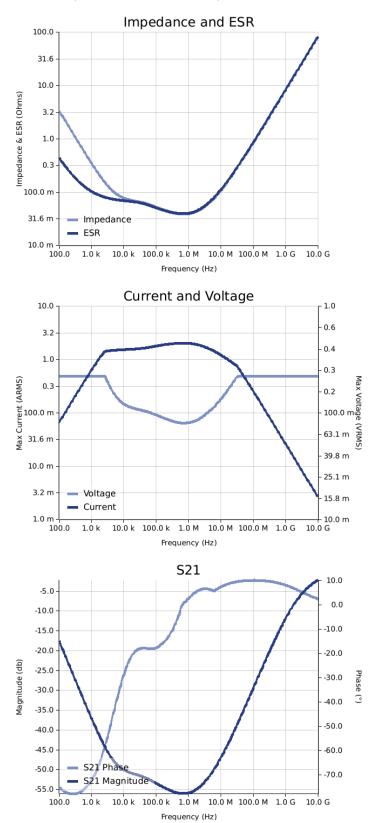
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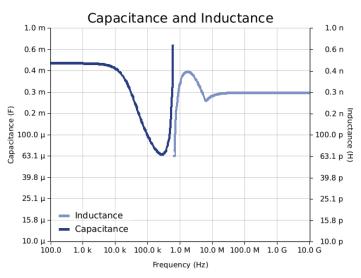


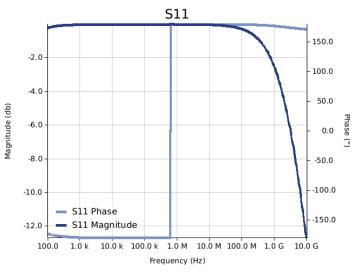
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Simulations

For the complete simulation environment please visit K-SIM.









These are simulations.

This is not a specification!

The responses shown represent the typical response for each part type. Specific responses may vary, depending on manufacturing variation affects of all parameters involved, including the specified tolerances applied to capacitance and unspecified variations of ESR, ESL, and leakage resistance.

The responses shown do not represent a specified or implied maximum capability of the device for all applications.

- The ESR used for ripple "Ripple Current/Voltage vs. Frequency" plots is the ESR at ambient temperature.

- The ESR used for hipple klipple current younge vs. requericy plots is the ESR at an bient temperature.
 The ESR in the "Temperature Rise vs. Ripple Current" plots is adjusted to each incremental temperature rise before the power and ripple current is calculated.
 The effects shown herein are based on measured data from a multiple part sample of the parts in question.
 Ripple capability of this device will be factored by thermal resistance (Rth) created by circuit traces (addi affects of all parameters involved, including the specified tolerances applied to capacitance and unspecified variations of ESR, ESL, and leakage resistance.
 The peak voltages generated in the "Temperature Rise vs. Combined Ripple Currents" plot are calculated for each frequency and are not combined with voltages and the part of the parts of the part of the
- generated at any other harmonics.
 Please consult with the catalog or field applications engineer for maximum capability of the device in specific applications.

All product information and data (collectively, the "Information") are subject to change without notice.

KEMET K-SIM is designed to simulate behavior of components with respect to frequency, ambient temperature, and DC bias levels. The responses shown represent the typical response for each part type. Specific responses may vary, depending on manufacturing variation effects of all parameters involved, including the specified tolerances applied to capacitance and unspecified variations of ESR, ESL, and leakage resistance.

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If you have any questions please contact K-SIM.